The Institute for Interconnecting and Packaging Electronic Circuits
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IPC-TM-650 TEST METHODS MANUAL

- **1 Scope** This test method determines electrical resistance of multilayer PWBs.
- 2 Applicable Documents None
- 3 Test Specimen
- 3.1 Test coupon "G"
- 4 Equipment/Apparatus
- 4.1 A four-terminal Kelvin Bridge or equivalent
- 5 Procedure
- 5.1 Test
- **5.1.1** Measure the resistance between any two adjacent holes in row A or E of Specimen G, except holes A5 & 6, E3, 4, 11, and 12 (see Figure 1).

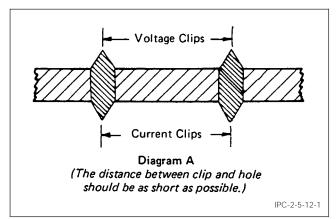


Figure 1 Distance from Clips

Number	
2.5.12	
Subject	
Interconnection Resistance, Multilayer Printed	
Wiring	
Date	Revision
4/73	
Originating Task Group	
N/A	

- **5.1.2** Solder leads in the selected holes for proper connection to the four terminals of the test instrument according to diagram A.
- **5.1.3** Repeat this test for each of the five pairs of holes.
- **5.2 Evaluation** Record and report all electrical resistance values.