

IPC-8952

Design Standard for Printed Electronics on Coated or Treated Textiles and E-Textiles

Developed by the E-Textiles Printed Electronics Design Standard Task Group (D-73a) of the E-Textiles Committee (D-70) of IPC

Users of this publication are encouraged to participate in the development of future revisions.

Contact:

IPC

Tel 847 615.7100 Fax 847 615.7105

Table of Contents

1 1.1	SCOPE 1 Purpose 1	3.4.1	Documentation and Identification of Formed Electrical Components	. 8
1.2	Classification	3.5	Test Requirement Considerations	. 8
1.3	Measurement Units	3.5.1	Electrical	. 8
1.4	Definition of Requirements	3.5.1.1	Bare Printed Electronic Testing	. 8
1.5	Process Control Requirements	3.5.1.2	Test Methods	. 9
1.6	Order of Precedence	3.5.1.2.1	HiPot Testing	10
1.6.1	Conflict	3.5.1.2.2	Impedance Considerations	11
1.6.2		3.5.1.2.3	Test Data (Source Data)	11
1.6.3		3.5.1.2.4	Interface Considerations	11
1.6.3		3.5.1.2.5	In-Processes Testing	11
1.6.4	Appendices	3.5.2	Printed Electronics E-Textile Assembly Testability	11
1.7	Quality Conformance	3.5.3	Functional Testing (FCT)	
1.8	Abbreviations and Acronyms 2	3.5.4	Test Points and Connectors	
1.9	Terms and Definitions	3.6	Layout Evaluation	13
1.9.1	Bonded Textile	3.6.1	Layout Design	
1.9.2	Textile	3.6.1.1	Layout Concepts	
1.9.3	,	3.6.2	Feasibility Density Evaluation	
1.9.4	Textile, Laminated		TERIALS	
1.9.5	,	4 IVIA 4.1	Material Selection	
1.9.6	Textile Substrate 2	4.1.1	Substrate Material Options	
1.10	Printed Electronics E-Textiles Types 2	4.1.1	-	14
1.11	Standard Printed Electronics Design (SPED) Classifications		Importance of T _g and T _m for Thermoplastic Textiles	
1.11.	1 SPED1	4.1.2	Functional Material Options	
1.11.	2 SPED2	4.1.3	Coatings and Material Surface Treatments	
1.11.	3 SPED3	4.2	Flexibility	
1.11.4	4 All SPEDs Example	4.2.1	Stretchability	16
2	APPLICABLE DOCUMENTS5	4.2.2	Bending	
2.1	IPC	4.2.3	Twisting	
2.2	ASTM	4.2.4	Crease and Crumple	
2.3	NCSL 6	4.3	Gap Bridging Applications	
2.4	SAE International 6	4.4	Printing Over Seam Structures	17
	GENERAL REQUIREMENTS	4.5	Conductive Interfaces and Out-of-Plane Interconnects	17
3		16		1 /
3.1	End-Product Performance Requirements 6	4.6	Via Hole Aspect Ratio/Material Deposit Aspect Ratio	18
3.2	Design Considerations	4.7	Process Compatibility	
3.2.1	Disposal and End-of-Life (EOL) Requirements	4.8	High-Aspect-Ratio Printing	
3.3	Schematic/Logic Diagram	4.9	Materials Deposition Methods	

4.9.2	Digital Printing Methods 20	4.16	Placed Components
4.9.3	Dispense Method Considerations 20	4.17	Marking and Legend
4.9.3.1	Nozzle Diameter	5 MEC	HANICAL AND PHYSICAL PROPERTIES 25
4.9.3.2	Print Speed	5.1	Fabrication Requirements
4.9.3.3	Nozzle Pressure	5.1.1	Sheet Form
4.9.3.4	Ink Viscosity and Nanoparticle Size 20	5.1.2	Roll-to-Roll
4.9.3.5	Substrate Wettability	5.1.3	Roll Goods
4.9.3.6	Substrate Roughness	5.2	Product/Printed Electronics E-Textile
4.9.3.7	Standoff		Configuration
4.9.4	Contact Resistance	5.2.1	Circuit Profile (Outline)
4.9.5	Compatibility of Inks to Inks	5.2.1.1	Hole-to-Edge Distance
4.9.6	Compatibility of Inks to Substrates 21	5.2.1.2	Strain Relief
4.9.7	Ink Properties	5.2.2	Bend, Flex, Twist and Stretch Areas 25
4.10	Dielectric Materials	5.2.2.1	Considerations to Withstand Mechanical
4.10.1	Dielectric Filaments		Stress and Strain
4.10.2	Dielectric Inks Materials 21	5.2.2.2	Stress Concentration Considerations 26
4.11	Adhesives	5.2.2.3	Calculating Bend Radius
4.11.1	Adhesives (Liquid)21	5.2.3	Forming Bends
4.11.2	Flexible Adhesive Bonding Films	5.2.3.1	Bends or Folds (Greater than 90°)27
	(Dry-Film Adhesive)	5.2.3.2	Bend Radius
4.11.3	Pressure-Sensitive Adhesives (PSAs) 22	6 ELEC	CTRICAL PROPERTIES
4.12	Conductive Materials-Based Systems 22	6.1	Electrical Considerations
4.12.1	Conductive Inks Functioning	6.1.1	Electrical Performance
4 12 2	by Percolation	6.1.2	Power Distribution Considerations 28
4.12.2	Conductive Inks Functioning by Sintering 22	6.1.3	Circuit Type Considerations 28
4.12.3	Conductive Filaments (Wires, Coated Wires or Conductive Filaments)	6.1.3.1	Digital Circuits
4.12.4	Conductive Films and Grids	6.1.3.2	Analog Circuits 29
4.12.5	Printed Conductive Seed Layers for Plating	6.2	Conductive Material Requirements 29
2.0	(Print and Plate)	6.3	Electrical Clearance
4.12.6	Isotropic Conductive Adhesives 23	6.4	Impedance Controls
4.12.7	Anisotropic Conductive Adhesives 23	6.5	Formed Components
4.12.8	Operations Following Plating 24	6.5.1	Formed Resistors
4.13	Coatings Over Printed Electronics24	6.5.2	Formed Capacitors
4.13.1	Electromigration Protective Coating 24	6.5.3	Formed Inductors
4.13.2	Conductive Coatings for Shielding 24	6.5.4	Formed Active Components
4.13.3	Organic Protective Coatings	7 THE	RMAL MANAGEMENT
4.13.4	Conformal Coating, Spray Coats 24	7.1	Cooling Mechanisms
4.14	Other Cover Materials	7.1.1	Conduction
4.14.1	Coverlay	7.1.2	Radiation
4.14.2	Coverfilm	7.1.3	Convection
4.14.3	Covercoat	7.1.4	Altitude Effects
4.15	Other Printed Materials	7.1.4	Heat Dissipation Considerations
		1.4	Tion Dissipation Considerations

7.2.1	Thermal Load Transmitted to Human Body 32	9.2.3	Model and Drawing Notes	. 37
8 C	OMPONENT AND ASSEMBLY ISSUES32	9.2.4	Automated-Layout Techniques	. 37
8.1	Lands for Surface-Mount Components 32	9.3	Deviation Requirements.	. 37
8.2	Constraints on Mounting to	9.4	Phototool Considerations	. 37
	Flexible Sections	9.4.1	Artwork Master Files	. 37
8.3	General Placement Requirements	9.4.2	Coating Phototools	. 37
8.3.1	Automatic Assembly	10 QU <i>A</i>	ALITY ASSURANCE	. 38
8.3.1.1	Printed Substrate Size	10.1	Material Quality Assurance	
8.3.1.2	Component Placement	10.2	Statistical Process Control (SPC)	
8.3.1.3	Component Operating Conditions	10.3	Build and Manufacturing Controls	
8.3.2	Orientation	10.4	Conformance Test Coupons	
8.3.3	Accessibility	10.4.1	Individual Coupon Design	
8.3.4	Design Envelope	10.4.2	Coupon Quantity and Location	
8.3.5	Flush Mounting Over Conductive Areas 34	10.4.3	Process Control Test Coupon	
8.3.6	Clearances	10.4.4	Tolerances and Reporting Data	
8.3.7	Physical Support	10.4.5	Coupon Identification	
8.3.7.1	Filleting	10.5	Responsibility for Inspection	
8.3.7.2	Mechanical Support for Surface Mounted	10.6	Test Equipment and Inspection Facilities	
	Components	10.7	Preparation of Samples	
8.3.8	Stress Relief	10.8	Standard Laboratory Conditions	
8.4	General Attachment Requirements	10.9	Tolerances	
8.4.1	Thermal Processing Considerations	10.10	Qualification Inspection	
8.4.2	Fastening Hardware	10.11	Failures.	
8.4.3	Encapsulation	10.12	User Sampling Plan	. 40
8.4.4	Component Selection Considerations 36	10.13	Noncompliance	
8.4.4.1	Component Sizes	10.14	Reduction of Quality Conformance Testing	
8.4.4.2	Component Thickness	10.15	Inspection Methodology	
8.4.4.3	Polarity Markings	10.15.1	Process Verification Inspection	
8.4.4.4	Reliability/Mounting Concerns	10.15.2	Visual Inspection	
8.4.4.5	Inspection	10.15.3	Magnification Aids	
8.4.4.6	Embedded Devices	10.15.4	Acceptance and Test Activities	
9 D	OCUMENTATION	10.15.4.1	Other Nondestructive Tests	
9.1	Special Tooling	10.16	Storage Conditions.	
9.2	Layout		A Index of Acronyms	
9.2.1	Viewing			
9.2.2	Accuracy and Scale			

IPC-8952 December 2022

	Figures	Figure 4-9	Printed Conductive Network and
Figure 1-1	SPED14		Microstructure for Polymer Thick Film
Figure 1-2	SPED2 4		(PTF) Inks (Top) and Metalorganic Inks (Bottom)
Figure 1-3	SPED3	Figure 5-1	Printed Connector I-Beaming (Left) vs
Figure 1-4	Example of Printed Electronic Using Every		Staggered (Right)
	Standard Printed Electronic Design (SPED) Type in One Device	Figure 5-2	Irregular Folds in Printed Electronics E-Textiles
Figure 3-1	Rounded Probe and Pin Probe	Figure 6-1	Voltage/Ground Distribution Concepts 28
Figure 4-1	Cross-Sectional View of a Representative	Figure 8-1	Printed Conductor-to-Hole Interface 35
	Construction Identifying Material Types 13	Figure 10-1	Systematic Path for Implementation of
Figure 4-2	Meander Pattern		Statistical Process Control (SPC) 39
Figure 4-3	Secondary Support Materials for Gap Bridging		Tables
Figure 4-4	Printing Over Stitched Seams	Table 3-1	Tests for Product Classes 9
Figure 4-5	Printed Over Bonded Seams	Table 3-2	Comparisons of Test and Inspection Methods
Figure 4-6	Examples of Ramps	Table 4-1	Deposition Methods
Figure 4-7	Hole Aspect Ratio		•
Figure 4-8	Material Deposit Aspect Ratio	Table 8-1	Printed Electronics E-Textiles Encapsulation Techniques

December 2022 IPC-8952

Design Standard for Printed Electronics on Coated or Treated Textiles and E-Textiles

1 SCOPE

This standard establishes specific requirements for the design of printed electronic applications and their forms of component mounting and interconnecting structures on coated or treated textile substrates. Textile substrate, as pertains to this standard, could be a bare textile or an integrated e-textile (e.g., woven or knitted e-textile).

Coated or treated textile substrates, as pertain to this standard, are textile substrates which have or will have a coating or treatment localized or across the full substrate.

1.1 Purpose The purpose of this standard is to establish specific design details, materials, test requirements, mechanical properties, physical properties, thermal management, interconnections and quality assurance for printed electronics on coated or treated textile substrates.

Coatings and treatments may be applied for printability of the textile substrate and/or for performance of the textile substrate or finished printed electronics e-textile (e.g., hydrophobic, water retardance, flame retardance, surface energy). Coatings or treatments may be applied using printing, lamination or other processes.

This standard does not cover printed electronics on nontextile substrates that may have some amount of pliability or stretchabilty (e.g., stretchable films).

1.2 Classification IPC standards recognize that electrical and electronic assemblies are subject to classifications by intended end-item use. Three general end-product classes have been established to reflect differences in manufacturability, complexity, functional performance requirements, and verification (inspection/test) frequency. It should be recognized that there may be overlaps of equipment between classes.

CLASS 1 General Electronic Products

Includes products suitable for applications where the major requirement is function of the completed assembly.

CLASS 2 Dedicated Service Electronic Products

Includes products where continued performance and extended life is required, and for which uninterrupted service is desired but not critical. Typically, the end-use environment would not cause failures.

CLASS 3 High Performance/Harsh Environment Electronic Products

Includes products where continued high performance or performance-on-demand is critical, equipment downtime cannot be tolerated, end-use environment may be uncommonly harsh, and the equipment must function when required, such as life support or other critical systems.

- **1.3 Measurement Units** All dimensions and tolerances in this specification are expressed in hard SI (metric) units and bracketed soft imperial [inch] units. Users of this specification are expected to use metric dimensions. All dimensions ≥ 1 mm [0.0394 in] will be expressed in millimeters and inches. All dimensions ≤ 1 mm [0.0394 in] will be expressed in micrometers and microinches.
- **1.4 Definition of Requirements** The words **shall or shall not** are used in the text of this document wherever there is a requirement for materials, preparation, process control or acceptance.

The word "should" reflects recommendations and is used to reflect general industry practices and procedures for guidance only.

Line drawings and illustrations are depicted herein to assist in the interpretation of the written requirements of this Standard. The text takes precedence over the figures.

1.5 Process Control Requirements The primary goal of process control is to continually reduce variation in the processes, products, or services to provide products or processes meeting or exceeding user requirements. Process control tools such as IPC-9191 or other user-approved system may be used as guidelines for implementing process control.

A documented process control system, if established, **shall** define process control and corrective action limits.

This may or may not be a statistical process control system. The use of statistical process control (SPC) is optional and should be based on factors such as design stability, lot size, production quantities and the needs of the manufacturer (see 11.2).

When a decision or requirement is to use a documented process control system, failure to implement process corrective action and/or the use of continually ineffective corrective actions **shall** be grounds for disapproval of the process and associated documentation.

IPC-8952 December 2022

1.6 Order of Precedence The contract **shall** take precedence over this standard, referenced standards and drawings.

In the event of conflict, the following order of precedence applies:

- 1) Procurement as agreed and documented between user and supplier.
- 2) Master drawing, design brief or tech pack reflecting the user's detailed requirements.
- 3) When invoked by the customer or per contractual agreement, this standard.

When documents other than this standard are cited, the order of precedence **shall** be defined in the procurement documents. The user can specify alternate acceptance criteria.

1.6.1 Conflict In the event of conflict between the requirements of this standard and the applicable drawing(s) and documentation, the applicable user-approved drawing(s) and documentation govern.

Some examples of documentation include the contract, purchase order, technical data package, engineering specification or performance specification. In the event of a conflict between the text of this standard and the applicable documents cited herein, the text of this standard takes precedence. In the event of conflict between the requirements of this standard and drawing(s) and documentation that has not been user approved, this standard governs.

- **1.6.2 Clause References** When a clause in this document is referenced, its subordinate clauses apply, unless the requirement references specific subordinate clauses.
- **1.6.3 Procurement Documentation** The procurement documentation **shall** provide sufficient information to the supplier so the supplier can produce printed boards containing embedded active and passive circuitry and ensure the user receives the desired product. The procurement documentation should specify the requirements that can be selected from within this standard.
- **1.6.3.1 Selection for Procurement** For procurement purposes, performance class **shall** be specified in the procurement documentation. The documentation **shall** provide sufficient information to the supplier so that one can fabricate the printed board and ensure that the user receives the desired product. Information that should be included in the procurement documentation is shown in IPC-D-325.
- **1.6.4 Appendices** Appendices to this standard are not binding requirements unless separately and specifically required by this standard, the applicable contracts, assembly drawing(s), documentation or purchase orders.
- **1.7 Quality Conformance** This standard establishes a quality conformance system for suppliers to demonstrate the continual conformance of materials to the quality requirements of the standard. See Section 4 for quality conformance inspection requirements.
- **1.8 Abbreviations and Acronyms** When used, periodic table elements are abbreviated in the standard. See Appendix A for full spellings of abbreviations (including elements) and acronyms used in this standard.
- 1.9 Terms and Definitions Terms and definitions shall be in accordance with IPC-T-50, IPC-T-51 and the following.
- **1.9.1 Bonded Textile** A layered fabric structure wherein a face or shell fabric is joined to a backing fabric, such as tricot, with an adhesive that does not significantly add to the thickness of the combined fabrics. Will pull this if it is not referenced in the standard.
- **1.9.2 Textile** A structure made from any combination of natural or manufactured fibers, having either a measurable staple length or a filament having a continuous length that is woven, nonwoven, braided, plaited, knitted, entangled, twisted or otherwise transformed into a flexible planar configuration.
- **1.9.3 Textile, Coated** Textiles which have additional layers, commonly polymeric, applied to one or both sides via a liquid or aerosol deposition process (direct coating, transfer coating, spray coating, extrusion coating, foam coating, etc.).
- **1.9.4 Textile, Laminated** A textile in which one or both sides have been bonded to a continuous sheet or film material, commonly polymeric.
- **1.9.5 Textile, Treated** A textile which has undergone a mechanical, chemical, or thermal process intended to improve specific characteristics of the textile.
- **1.9.6 Textile Substrate** A textile upon which an electronic structure is built, laminated or otherwise integrated. Textile substrates relevant to this standard may be coated or laminated prior to electronic structure integration.
- **1.10 Printed Electronics E-Textiles Types** Any printed electronics e-textiles design will be incumbent on requirements from the customer, materials to be used and the printing processes. The following printed electronics types represent the known variations