

**IPC-7093A** 

# Design and Assembly Process Implementation for Bottom Termination Components (BTCs)

Developed by the Bottom Termination Components (BTC) Task Group (5-21h) of the Assembly and Joining Committee (5-20) of IPC

Users of this publication are encouraged to participate in the development of future revisions.

Contact:

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## **Table of Contents**

| 1 SC                                                                                                                                                                                                                                            | <b>COPE</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 1.1                                                                                                                                                                                                                                             | Purpose 1                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| 1.2                                                                                                                                                                                                                                             | Classification 1                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| 1.3                                                                                                                                                                                                                                             | Measurement Units 1                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| 1.4                                                                                                                                                                                                                                             | Definition of Requirements 1                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| 1.5                                                                                                                                                                                                                                             | Process Control Requirements 2                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| 1.6                                                                                                                                                                                                                                             | Order of Precedence 2                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| 1.6.1                                                                                                                                                                                                                                           | Clause References 2                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| 1.7                                                                                                                                                                                                                                             | Use of "Lead" 2                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| 1.8                                                                                                                                                                                                                                             | Abbreviations and Acronyms 2                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| 1.9                                                                                                                                                                                                                                             | Terms and Definitions 2                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| 1.9.1                                                                                                                                                                                                                                           | Bottom Termination Component (BTC) 2                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| 1.9.2                                                                                                                                                                                                                                           | Wettable Side Flank 2                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| 1.9.3                                                                                                                                                                                                                                           | Exposed Die Paddle 2                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| 1.9.4                                                                                                                                                                                                                                           | Thermal Pad 2                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| 1.9.5                                                                                                                                                                                                                                           | Thermal Via 2                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| 2 AF                                                                                                                                                                                                                                            | PPLICABLE DOCUMENTS                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| 2.1                                                                                                                                                                                                                                             | IPC                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| 2.2                                                                                                                                                                                                                                             | Joint Standards                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| 2.3                                                                                                                                                                                                                                             | ASME                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| 2.4                                                                                                                                                                                                                                             | IEC                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| 2.5                                                                                                                                                                                                                                             |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| 2.3                                                                                                                                                                                                                                             | JEDEC                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| 2.5                                                                                                                                                                                                                                             |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| 2.3<br>3 GI                                                                                                                                                                                                                                     | ENERAL OVERVIEW OF BTCs                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <ul> <li>2.3</li> <li>3 GI</li> <li>3.1</li> <li>2.1 1</li> </ul>                                                                                                                                                                               | JEDEC       4         ENERAL OVERVIEW OF BTCs       5         BTC Types       5                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <ul> <li>2.3</li> <li>3 GI</li> <li>3.1</li> <li>3.1.1</li> <li>2.1.2</li> </ul>                                                                                                                                                                | JEDEC       4         ENERAL OVERVIEW OF BTCs       5         BTC Types       5         BTCs with Thermal Die Paddle       5                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>3 GF</b><br>3.1<br>3.1.1<br>3.1.2                                                                                                                                                                                                            | <b>ENERAL OVERVIEW OF BTCs</b> 5         BTC Types       5         BTCs with Thermal Die Paddle       5         BTCs without Thermal Die Paddle       5                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| <b>3 GF</b><br>3.1<br>3.1.1<br>3.1.2<br>3.1.3                                                                                                                                                                                                   | JEDEC4ENERAL OVERVIEW OF BTCs5BTC Types5BTCs with Thermal Die Paddle5BTCs without Thermal Die Paddle5Flip Chip QFN (FC-QFN)5                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| <b>3 G</b><br><b>3</b> .1<br>3.1.1<br>3.1.2<br>3.1.3<br>3.1.4                                                                                                                                                                                   | JEDEC4ENERAL OVERVIEW OF BTCs5BTC Types5BTCs with Thermal Die Paddle5BTCs without Thermal Die Paddle5Flip Chip QFN (FC-QFN)5Land Grid Array (LGA)5                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| <b>3 G</b><br>3.1<br>3.1.1<br>3.1.2<br>3.1.3<br>3.1.4<br>3.1.5                                                                                                                                                                                  | JEDEC4ENERAL OVERVIEW OF BTCs5BTC Types5BTCs with Thermal Die Paddle5BTCs without Thermal Die Paddle5Flip Chip QFN (FC-QFN)5Land Grid Array (LGA)5Other BTC Designations5                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>3 GH</b><br>3.1<br>3.1.1<br>3.1.2<br>3.1.3<br>3.1.4<br>3.1.5<br>3.1.6                                                                                                                                                                        | JEDEC4ENERAL OVERVIEW OF BTCs5BTC Types5BTCs with Thermal Die Paddle5BTCs without Thermal Die Paddle5Flip Chip QFN (FC-QFN)5Land Grid Array (LGA)5Other BTC Designations5BTC Device Manufacturer Acronyms6                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| <b>3 G</b><br>3.1<br>3.1.1<br>3.1.2<br>3.1.3<br>3.1.4<br>3.1.5<br>3.1.6<br>3.1.7                                                                                                                                                                | JEDEC4ENERAL OVERVIEW OF BTCs5BTC Types5BTCs with Thermal Die Paddle5BTCs without Thermal Die Paddle5Flip Chip QFN (FC-QFN)5Land Grid Array (LGA)5Other BTC Designations5BTC Device Manufacturer Acronyms6Description of Different Component5Structures6                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| <b>3 GH</b><br>3.1<br>3.1.1<br>3.1.2<br>3.1.3<br>3.1.4<br>3.1.5<br>3.1.6<br>3.1.7<br>3.2                                                                                                                                                        | JEDEC4ENERAL OVERVIEW OF BTCs5BTC Types5BTCs with Thermal Die Paddle5BTCs without Thermal Die Paddle5Flip Chip QFN (FC-QFN)5Land Grid Array (LGA)5Other BTC Designations5BTC Device Manufacturer Acronyms6Description of Different Component5Structures6Total Cost of Ownership9                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| <b>3 G</b><br>3.1<br>3.1.1<br>3.1.2<br>3.1.3<br>3.1.4<br>3.1.5<br>3.1.6<br>3.1.7<br>3.2<br>3.3                                                                                                                                                  | JEDEC4ENERAL OVERVIEW OF BTCs5BTC Types5BTCs with Thermal Die Paddle5BTCs without Thermal Die Paddle5Flip Chip QFN (FC-QFN)5Land Grid Array (LGA)5Other BTC Designations5BTC Device Manufacturer Acronyms6Description of Different Component5Structures6Total Cost of Ownership9Design and Assembly Process Considerations9                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>3 G</b><br><b>3 G</b><br><b>3</b> .1<br><b>3</b> .1.1<br><b>3</b> .1.2<br><b>3</b> .1.3<br><b>3</b> .1.4<br><b>3</b> .1.5<br><b>3</b> .1.6<br><b>3</b> .1.7<br><b>3</b> .2<br><b>3</b> .3<br><b>3</b> .3.1                                   | JEDEC4ENERAL OVERVIEW OF BTCs5BTC Types5BTCs with Thermal Die Paddle5BTCs without Thermal Die Paddle5Flip Chip QFN (FC-QFN)5Land Grid Array (LGA)5Other BTC Designations5BTC Device Manufacturer Acronyms6Description of Different Component5Structures6Total Cost of Ownership9Design and Assembly Process Considerations9Current-Carrying Requirements and Thermal9                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| <b>3 G</b><br><b>3 G</b><br><b>3</b> .1<br>3.1.1<br>3.1.2<br>3.1.3<br>3.1.4<br>3.1.5<br>3.1.6<br>3.1.7<br>3.2<br>3.3<br>3.3.1<br>3.3.2                                                                                                          | JEDEC4ENERAL OVERVIEW OF BTCs5BTC Types5BTCs with Thermal Die Paddle5BTCs without Thermal Die Paddle5Flip Chip QFN (FC-QFN)5Land Grid Array (LGA)5Other BTC Designations5BTC Device Manufacturer Acronyms6Description of Different Component5Structures6Total Cost of Ownership9Design and Assembly Process Considerations9Current-Carrying Requirements and Thermal9Electrical Performance10                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| <b>3 G</b><br><b>3 G</b><br><b>3</b> .1<br><b>3</b> .1.1<br><b>3</b> .1.2<br><b>3</b> .1.3<br><b>3</b> .1.4<br><b>3</b> .1.5<br><b>3</b> .1.6<br><b>3</b> .1.7<br><b>3</b> .2<br><b>3</b> .3<br><b>3</b> .3.1<br><b>3</b> .3.2<br><b>3</b> .3.3 | JEDEC       4         ENERAL OVERVIEW OF BTCs       5         BTC Types       5         BTCs with Thermal Die Paddle       5         BTCs without Thermal Die Paddle       5         Flip Chip QFN (FC-QFN)       5         Land Grid Array (LGA)       5         Other BTC Designations       5         BTC Device Manufacturer Acronyms       6         Description of Different Component       5         Structures       6         Total Cost of Ownership       9         Design and Assembly Process Considerations       6         for QFN Type BTC Packages       9         Current-Carrying Requirements and Thermal       9         Electrical Performance       10         Selection of Solder Paste Type, Stencil       10         Design and Solder Paste Printing       10                                                                                                                                                                                                                                                                                                                                                                      |
| <b>3 G</b><br><b>3 G</b><br><b>3</b> .1<br>3.1.1<br>3.1.2<br>3.1.3<br>3.1.4<br>3.1.5<br>3.1.6<br>3.1.7<br>3.2<br>3.3<br>3.3.1<br>3.3.2<br>3.3.3<br>3.3.4                                                                                        | JEDEC       4         ENERAL OVERVIEW OF BTCs       5         BTC Types       5         BTCs with Thermal Die Paddle       5         BTCs without Thermal Die Paddle       5         BTC hip QFN (FC-QFN)       5         Land Grid Array (LGA)       5         Other BTC Designations       5         BTC Device Manufacturer Acronyms       6         Description of Different Component       5         Structures       6         Total Cost of Ownership       9         Design and Assembly Process Considerations       6         for QFN Type BTC Packages       9         Current-Carrying Requirements and Thermal       9         Dissipation       9         Electrical Performance       10         Selection of Solder Paste Type, Stencil       10         Design and Solder Paste Printing       10         Single Thermal Pad Design       10 |
| <b>3 G</b><br><b>3 G</b><br><b>3</b> .1<br>3.1.1<br>3.1.2<br>3.1.3<br>3.1.4<br>3.1.5<br>3.1.6<br>3.1.7<br>3.2<br>3.3<br>3.3.1<br>3.3.2<br>3.3.3<br>3.3.4<br>3.3.5                                                                               | JEDEC       4         ENERAL OVERVIEW OF BTCs       5         BTC Types       5         BTCs with Thermal Die Paddle       5         BTCs without Thermal Die Paddle       5         Flip Chip QFN (FC-QFN)       5         Land Grid Array (LGA)       5         Other BTC Designations       5         BTC Device Manufacturer Acronyms       6         Description of Different Component       5         Structures       6         Total Cost of Ownership       9         Design and Assembly Process Considerations       7         for QFN Type BTC Packages       9         Current-Carrying Requirements and Thermal       10         Dissipation       9         Electrical Performance       10         Selection of Solder Paste Type, Stencil       10         Design and Solder Paste Printing       10         Multiple-Thermal-Pads Design       10                                                                                                                                                                                                                                                                                           |
| 3       GI         3.1       3.1.1         3.1.2       3.1.3         3.1.3       3.1.4         3.1.5       3.1.6         3.1.7       3.2         3.3       3.3.1         3.3.2       3.3.3         3.3.4       3.3.5         3.3.6       3.3.6  | JEDEC       4         ENERAL OVERVIEW OF BTCs       5         BTC Types       5         BTCs with Thermal Die Paddle       5         BTCs without Thermal Die Paddle       5         BTC Support       5         Land Grid Array (LGA)       5         Other BTC Designations       5         BTC Device Manufacturer Acronyms       6         Description of Different Component       5         Structures       6         Total Cost of Ownership       9         Design and Assembly Process Considerations       6         for QFN Type BTC Packages       9         Current-Carrying Requirements and Thermal       9         Electrical Performance       10         Selection of Solder Paste Type, Stencil       9         Design and Solder Paste Printing       10         Single Thermal Pad Design       10         Multiple-Thermal-Pads Design       10         Wettable Side Flank       10    |

| 3.4.1       Quad Flat No-Lead (QFN) Solder         Joint Inspection       11         3.4.2       Automated X-Ray Inspection (AXI)       11         3.4.3       Failure Analysis       12         3.4.4       Rework Considerations       12         3.4.4       Rework Considerations       12         4       COMPONENT CONSIDERATIONS       14         4.1       General Description of Different BTC       Packages         9       Packages       14         4.2       Detailed Description and Standards for       BTCs         9       BTCs       16         4.2.1       Single-Row Molded Lead-Frame-Based       Packaging         9       Packaging       16         4.2.2       Multiple-Row Molded Lead-Frame-Based       Packaging         9       Packages       16         4.2.3       Plastic-Quad, Dual-Inline, Square and<br>Rectangular No-Lead Staggered Dual-Row       Packages         4.2.4       Punch-Singulated Fine Pitch Square Very       Thin and Ultra-Thin Profile Lead-Frame-Based         9       Packages       20         4.2.5       Quad No-Lead Staggered and In-Line       Multiple-Row Packages         4.3       Detailed Description of Quad Flat No-Lead       (SON) / <th>3.4</th> <th>Inspection and Rework</th> <th>11</th>                                                                                                                                                                          | 3.4   | Inspection and Rework                                                                                                                   | 11 |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------|-----------------------------------------------------------------------------------------------------------------------------------------|----|
| 3.4.2       Automated X-Ray Inspection (AXI)       11         3.4.3       Failure Analysis       12         3.4.4       Rework Considerations       12         3.5       Needs and Expectations       12         4       COMPONENT CONSIDERATIONS       14         4.1       General Description of Different BTC       Packages         Packages       14         4.2       Detailed Description and Standards for BTCs       16         4.2.1       Single-Row Molded Lead-Frame-Based Packaging       16         4.2.2       Multiple-Row Molded Lead-Frame-Based Packaging       16         4.2.3       Plastic-Quad, Dual-Inline, Square and Rectangular No-Lead Packages       16         4.2.4       Punch-Singulated Fine Pitch Square Very Thin and Ultra-Thin Profile Lead-Frame-Based Quad No-Lead Staggered Dual-Row Packages       20         4.2.5       Quad No-Lead Staggered and In-Line Multiple-Row Packages       23         4.3       Detailed Description of Quad Flat No-Lead (QFN) and Small Outline No-Lead (SON) / Dual Flat No-Lead (DFN) Packages       26         4.3.2       Types of Defects       30       32         4.3.3       Matrials Used       32       32         4.3.4       Materials Used       32       33         4.3.5       Solderability                                                                                                                                                          | 3.4.1 | Quad Flat No-Lead (QFN) Solder<br>Joint Inspection                                                                                      | 11 |
| 3.4.3       Failure Analysis       12         3.4.4       Rework Considerations       12         3.5       Needs and Expectations       12         4       COMPONENT CONSIDERATIONS       14         4.1       General Description of Different BTC       Packages         Packages       14         4.2       Detailed Description and Standards for BTCs       16         4.2.1       Single-Row Molded Lead-Frame-Based Packaging       16         4.2.2       Multiple-Row Molded Lead-Frame-Based Packaging       16         4.2.3       Plastic-Quad, Dual-Inline, Square and Rectangular No-Lead Packages       16         4.2.4       Punch-Singulated Fine Pitch Square Very Thin and Ultra-Thin Profile Lead-Frame-Based Quad No-Lead Staggered Dual-Row Packages       20         4.2.5       Quad No-Lead Staggered and In-Line Multiple-Row Packages       23         4.3       Detailed Description of Quad Flat No-Lead (SON) / Dual Flat No-Lead (DFN) Packages       26         4.3.1       Manufacturing Methods       26         4.3.2       Types of Defects       30         4.3.3       Materials Used       32         4.3.4       Materials Used       32         4.3.5       Solderability Testing       32         4.4       Custom Quad Flat                                                                                                                                                                           | 3.4.2 | Automated X-Ray Inspection (AXI)                                                                                                        | 11 |
| 3.4.4       Rework Considerations       12         3.5       Needs and Expectations       12         4       COMPONENT CONSIDERATIONS       14         4.1       General Description of Different BTC       Packages         Packages       14         4.2       Detailed Description and Standards for<br>BTCs       16         4.2.1       Single-Row Molded Lead-Frame-Based<br>Packaging       16         4.2.2       Multiple-Row Molded Lead-Frame-Based<br>Packaging       16         4.2.3       Plastic-Quad, Dual-Inline, Square and<br>Rectangular No-Lead Packages       16         4.2.4       Punch-Singulated Fine Pitch Square Very<br>Thin and Ultra-Thin Profile Lead-Frame-<br>Based Quad No-Lead Staggered Dual-Row<br>Packages       20         4.2.5       Quad No-Lead Staggered and In-Line<br>Multiple-Row Packages       23         4.3       Detailed Description of Quad Flat No-Lead<br>(QFN) and Small Outline No-Lead (SON) /<br>Dual Flat No-Lead (DFN) Packages       26         4.3.1       Manufacturing Methods       26         4.3.2       Types of Defects       30         4.3.3       Materials Used       32         4.3.4       Materials Used       32         4.3.5       Solderability Testing       32         4.4       Custom Quad Flat No-Lead (QFN) and Small<br>Outline No-Lead (SON) / Dual Fla                                                                                              | 3.4.3 | Failure Analysis                                                                                                                        | 12 |
| 3.5       Needs and Expectations       12         4       COMPONENT CONSIDERATIONS       14         4.1       General Description of Different BTC       Packages         Packages       14         4.2       Detailed Description and Standards for<br>BTCs       16         4.2.1       Single-Row Molded Lead-Frame-Based<br>Packaging       16         4.2.2       Multiple-Row Molded Lead-Frame-Based<br>Packaging       16         4.2.3       Plastic-Quad, Dual-Inline, Square and<br>Rectangular No-Lead Packages       16         4.2.4       Punch-Singulated Fine Pitch Square Very<br>Thin and Ultra-Thin Profile Lead-Frame-<br>Based Quad No-Lead Staggered Dual-Row<br>Packages       20         4.2.5       Quad No-Lead Staggered and In-Line<br>Multiple-Row Packages       23         4.3       Detailed Description of Quad Flat No-Lead<br>(QFN) and Small Outline No-Lead (SON) /<br>Dual Flat No-Lead (DFN) Packages       26         4.3.2       Types of Defects       30         4.3.3       Materials Used       32         4.3.4       Materials Used       32         4.3.5       Solderability Testing       32         4.4       Custom Quad Flat No-Lead (QFN) and Small<br>Outline No-Lead (SON) / Dual Flat No-Lead<br>(DFN) Packaging       33         4.5.1       Description of Commercial Variations       33         4.                                                                                  | 3.4.4 | Rework Considerations                                                                                                                   | 12 |
| 4       COMPONENT CONSIDERATIONS       14         4.1       General Description of Different BTC<br>Packages       14         4.2       Detailed Description and Standards for<br>BTCs       16         4.2.1       Single-Row Molded Lead-Frame-Based<br>Packaging       16         4.2.2       Multiple-Row Molded Lead-Frame-Based<br>Packaging       16         4.2.3       Plastic-Quad, Dual-Inline, Square and<br>Rectangular No-Lead Packages       16         4.2.4       Punch-Singulated Fine Pitch Square Very<br>Thin and Ultra-Thin Profile Lead-Frame-<br>Based Quad No-Lead Staggered Dual-Row<br>Packages       20         4.2.5       Quad No-Lead Staggered and In-Line<br>Multiple-Row Packages       23         4.3       Detailed Description of Quad Flat No-Lead<br>(QFN) and Small Outline No-Lead (SON) /<br>Dual Flat No-Lead (DFN) Packages       26         4.3.1       Manufacturing Methods       26         4.3.2       Types of Defects       30         4.3.3       Marking Alternatives       32         4.3.4       Materials Used       32         4.3.5       Solderability Testing       32         4.4       Custom Quad Flat No-Lead (QFN) and Small<br>Outline No-Lead (SON) / Dual Flat No-Lead<br>(DFN) Packaging       33         4.5       Description of Commercial Variations       33         4.5.1       Detailed Description of Micro Lead                                                     | 3.5   | Needs and Expectations                                                                                                                  | 12 |
| 4.1       General Description of Different BTC         Packages       14         4.2       Detailed Description and Standards for<br>BTCs       16         4.2.1       Single-Row Molded Lead-Frame-Based<br>Packaging       16         4.2.2       Multiple-Row Molded Lead-Frame-Based<br>Packaging       16         4.2.2       Multiple-Row Molded Lead-Frame-Based<br>Packaging       16         4.2.3       Plastic-Quad, Dual-Inline, Square and<br>Rectangular No-Lead Packages       16         4.2.4       Punch-Singulated Fine Pitch Square Very<br>Thin and Ultra-Thin Profile Lead-Frame-<br>Based Quad No-Lead Staggered Dual-Row<br>Packages       20         4.2.5       Quad No-Lead Staggered and In-Line<br>Multiple-Row Packages       23         4.3       Detailed Description of Quad Flat No-Lead<br>(QFN) and Small Outline No-Lead (SON) /<br>Dual Flat No-Lead (DFN) Packages       26         4.3.1       Manufacturing Methods       26         4.3.2       Types of Defects       30         4.3.3       Marking Alternatives       32         4.3.4       Materials Used       32         4.3.5       Solderability Testing       32         4.4       Custom Quad Flat No-Lead (QFN) and Small<br>Outline No-Lead (SON) / Dual Flat No-Lead<br>(DFN) Packaging       33         4.5       Description of Commercial Variations       33                                                                          | 4 CC  | OMPONENT CONSIDERATIONS                                                                                                                 | 14 |
| 4.2       Detailed Description and Standards for<br>BTCs       16         4.2.1       Single-Row Molded Lead-Frame-Based<br>Packaging       16         4.2.2       Multiple-Row Molded Lead-Frame-Based<br>Packaging       16         4.2.3       Plastic-Quad, Dual-Inline, Square and<br>Rectangular No-Lead Packages       16         4.2.4       Punch-Singulated Fine Pitch Square Very<br>Thin and Ultra-Thin Profile Lead-Frame-<br>Based Quad No-Lead Staggered Dual-Row<br>Packages       20         4.2.5       Quad No-Lead Staggered and In-Line<br>Multiple-Row Packages       23         4.3       Detailed Description of Quad Flat No-Lead<br>(QFN) and Small Outline No-Lead (SON) /<br>Dual Flat No-Lead (DFN) Packages       26         4.3.1       Manufacturing Methods       26         4.3.2       Types of Defects       30         4.3.3       Marking Alternatives       32         4.3.4       Materials Used       32         4.3.5       Solderability Testing       32         4.4       Custom Quad Flat No-Lead (QFN) and Small<br>Outline No-Lead (SON) / Dual Flat No-Lead<br>(DFN) Packaging       33         4.5       Description of Commercial Variations       33         4.5.1       Detailed Description of Micro Lead Frame<br>(MLF), Micro Lead Package (MLP)<br>and Micro Lead Frame Plastic (MLFP)<br>Components       33         4.5.2       Detailed Description of Leadless Lead Frame<br>Package | 4.1   | General Description of Different BTC<br>Packages                                                                                        | 14 |
| <ul> <li>4.2.1 Single-Row Molded Lead-Frame-Based<br/>Packaging</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | 4.2   | Detailed Description and Standards for<br>BTCs                                                                                          | 16 |
| <ul> <li>4.2.2 Multiple-Row Molded Lead-Frame-Based<br/>Packaging</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       | 4.2.1 | Single-Row Molded Lead-Frame-Based Packaging                                                                                            | 16 |
| <ul> <li>4.2.3 Plastic-Quad, Dual-Inline, Square and<br/>Rectangular No-Lead Packages</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   | 4.2.2 | Multiple-Row Molded Lead-Frame-Based<br>Packaging                                                                                       | 16 |
| <ul> <li>4.2.4 Punch-Singulated Fine Pitch Square Very<br/>Thin and Ultra-Thin Profile Lead-Frame-<br/>Based Quad No-Lead Staggered Dual-Row<br/>Packages</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               | 4.2.3 | Plastic-Quad, Dual-Inline, Square and<br>Rectangular No-Lead Packages                                                                   | 16 |
| <ul> <li>4.2.5 Quad No-Lead Staggered and In-Line<br/>Multiple-Row Packages</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             | 4.2.4 | Punch-Singulated Fine Pitch Square Very<br>Thin and Ultra-Thin Profile Lead-Frame-<br>Based Quad No-Lead Staggered Dual-Row<br>Packages | 20 |
| <ul> <li>4.3 Detailed Description of Quad Flat No-Lead<br/>(QFN) and Small Outline No-Lead (SON) /<br/>Dual Flat No-Lead (DFN) Packages</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 | 4.2.5 | Quad No-Lead Staggered and In-Line<br>Multiple-Row Packages                                                                             | 23 |
| <ul> <li>4.3.1 Manufacturing Methods</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    | 4.3   | Detailed Description of Quad Flat No-Lead<br>(QFN) and Small Outline No-Lead (SON) /<br>Dual Flat No-Lead (DFN) Packages                | 26 |
| <ul> <li>4.3.2 Types of Defects</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | 4.3.1 | Manufacturing Methods                                                                                                                   | 26 |
| <ul> <li>4.3.3 Marking Alternatives</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     | 4.3.2 | Types of Defects                                                                                                                        | 30 |
| <ul> <li>4.3.4 Materials Used</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           | 4.3.3 | Marking Alternatives                                                                                                                    | 32 |
| <ul> <li>4.3.5 Solderability Testing</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    | 4.3.4 | Materials Used                                                                                                                          | 32 |
| <ul> <li>4.4 Custom Quad Flat No-Lead (QFN) and Small<br/>Outline No-Lead (SON) / Dual Flat No-Lead<br/>(DFN) Packaging</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 | 4.3.5 | Solderability Testing                                                                                                                   | 32 |
| <ul> <li>4.5 Description of Commercial Variations</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       | 4.4   | Custom Quad Flat No-Lead (QFN) and Small<br>Outline No-Lead (SON) / Dual Flat No-Lead<br>(DFN) Packaging                                | 33 |
| <ul> <li>4.5.1 Detailed Description of Micro Lead Frame<br/>(MLF), Micro Lead Package (MLP)<br/>and Micro Lead Frame Plastic (MLFP)<br/>Components</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      | 4.5   | Description of Commercial Variations                                                                                                    | 33 |
| <ul> <li>4.5.2 Detailed Description of Leadless Lead Frame<br/>Package (LLP) and Lead Frame Chip Scale<br/>Package (LFCSP) Components</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   | 4.5.1 | Detailed Description of Micro Lead Frame<br>(MLF), Micro Lead Package (MLP)<br>and Micro Lead Frame Plastic (MLFP)<br>Components        | 33 |
| <ul> <li>4.6 Land Grid Arrays (LGAs)</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    | 4.5.2 | Detailed Description of Leadless Lead Frame<br>Package (LLP) and Lead Frame Chip Scale<br>Package (LFCSP) Components                    | 36 |
| <ul> <li>4.6.1 Land Grid Array (LGA) Construction</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       | 4.6   | Land Grid Arrays (LGAs)                                                                                                                 | 38 |
| <ul> <li>4.6.2 Manufacturing Methods for Substrate-Based<br/>LGAs</li></ul>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       | 4.6.1 | Land Grid Array (LGA) Construction                                                                                                      | 39 |
| 4.6.3 Types of Defects 42                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | 4.6.2 | Manufacturing Methods for Substrate-Based LGAs                                                                                          | 41 |
|                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   | 4.6.3 | Types of Defects                                                                                                                        | 42 |

| 4.6.4                                                                                                                                                                                                                                                                                    | Assembly Challenges Mounting Land Grid<br>Array (LGA) Packages                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | 42                                                                                                                               |
|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------|
| 4.6.5                                                                                                                                                                                                                                                                                    | HDI Process                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     | 42                                                                                                                               |
| 5 PF<br>M                                                                                                                                                                                                                                                                                | RINTED BOARDS AND OTHER<br>OUNTING STRUCTURES                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   | 44                                                                                                                               |
| 5.1                                                                                                                                                                                                                                                                                      | High-Density Interconnect (HDI) Build-Up<br>Layers                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              | 44                                                                                                                               |
| 5.2                                                                                                                                                                                                                                                                                      | Base Materials Considerations                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   | 44                                                                                                                               |
| 5.3                                                                                                                                                                                                                                                                                      | Moisture Absorption                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             | 44                                                                                                                               |
| 5.4                                                                                                                                                                                                                                                                                      | Surface Finishes                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                | 44                                                                                                                               |
| 5.4.1                                                                                                                                                                                                                                                                                    | Hot-Air Solder Leveling (HASL)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | 46                                                                                                                               |
| 5.4.2                                                                                                                                                                                                                                                                                    | Organic Solderability Preservative (OSP)<br>Coatings                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            | 46                                                                                                                               |
| 5.4.3                                                                                                                                                                                                                                                                                    | Electroless Ni/Immersion Au (ENIG)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              | 46                                                                                                                               |
| 5.4.4                                                                                                                                                                                                                                                                                    | Electrolytic Ni/Electroplated Au (ENEG)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | 46                                                                                                                               |
| 5.4.5                                                                                                                                                                                                                                                                                    | Electroless Ni/Electroless Pd/Immersion Au (ENEPIG)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             | 47                                                                                                                               |
| 5.4.6                                                                                                                                                                                                                                                                                    | Immersion Ag                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    | 47                                                                                                                               |
| 5.4.7                                                                                                                                                                                                                                                                                    | Immersion Sn                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    | 47                                                                                                                               |
| 5.4.8                                                                                                                                                                                                                                                                                    | Solid Solder Deposition                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | 47                                                                                                                               |
| 5.5                                                                                                                                                                                                                                                                                      | Silk Screen                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     | 49                                                                                                                               |
| 5.6                                                                                                                                                                                                                                                                                      | Site Flatness, Bow and Twist                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    | 50                                                                                                                               |
| 6 PF                                                                                                                                                                                                                                                                                     | RINTED BOARD DESIGN CONSIDERATIONS                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              | 50                                                                                                                               |
|                                                                                                                                                                                                                                                                                          |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |                                                                                                                                  |
| 6.1                                                                                                                                                                                                                                                                                      | Design for Assembly Considerations                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              | 50                                                                                                                               |
| 6.1<br>6.2                                                                                                                                                                                                                                                                               | Design for Assembly Considerations<br>BTC Land Pattern Design Process                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           | 50<br>50                                                                                                                         |
| <ul><li>6.1</li><li>6.2</li><li>6.3</li></ul>                                                                                                                                                                                                                                            | Design for Assembly Considerations<br>BTC Land Pattern Design Process<br>Package Variations                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     | 50<br>50<br>51                                                                                                                   |
| <ul><li>6.1</li><li>6.2</li><li>6.3</li><li>6.4</li></ul>                                                                                                                                                                                                                                | Design for Assembly Considerations<br>BTC Land Pattern Design Process<br>Package Variations<br>BTC Land Pattern and Component Symbol<br>Coding                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | 50<br>50<br>51<br>52                                                                                                             |
| <ul><li>6.1</li><li>6.2</li><li>6.3</li><li>6.4</li><li>6.5</li></ul>                                                                                                                                                                                                                    | Design for Assembly Considerations<br>BTC Land Pattern Design Process<br>Package Variations<br>BTC Land Pattern and Component Symbol<br>Coding<br>Circuit Routing Considerations / Thermal<br>Via Keep-Outs                                                                                                                                                                                                                                                                                                                                                                                                     | <ul> <li>50</li> <li>50</li> <li>51</li> <li>52</li> <li>53</li> </ul>                                                           |
| <ul> <li>6.1</li> <li>6.2</li> <li>6.3</li> <li>6.4</li> <li>6.5</li> <li>6.6</li> </ul>                                                                                                                                                                                                 | Design for Assembly Considerations<br>BTC Land Pattern Design Process<br>Package Variations<br>BTC Land Pattern and Component Symbol<br>Coding<br>Circuit Routing Considerations / Thermal<br>Via Keep-Outs<br>Important BTC Package Elements for<br>Physical Design                                                                                                                                                                                                                                                                                                                                            | <ul> <li>50</li> <li>50</li> <li>51</li> <li>52</li> <li>53</li> <li>53</li> </ul>                                               |
| <ul> <li>6.1</li> <li>6.2</li> <li>6.3</li> <li>6.4</li> <li>6.5</li> <li>6.6</li> <li>6.6.1</li> </ul>                                                                                                                                                                                  | Design for Assembly Considerations<br>BTC Land Pattern Design Process<br>Package Variations<br>BTC Land Pattern and Component Symbol<br>Coding<br>Circuit Routing Considerations / Thermal<br>Via Keep-Outs<br>Important BTC Package Elements for<br>Physical Design<br>Termination Formats                                                                                                                                                                                                                                                                                                                     | <ul> <li>50</li> <li>50</li> <li>51</li> <li>52</li> <li>53</li> <li>53</li> <li>53</li> </ul>                                   |
| <ul> <li>6.1</li> <li>6.2</li> <li>6.3</li> <li>6.4</li> <li>6.5</li> <li>6.6</li> <li>6.6.1</li> <li>6.7</li> </ul>                                                                                                                                                                     | Design for Assembly Considerations<br>BTC Land Pattern Design Process<br>Package Variations<br>BTC Land Pattern and Component Symbol<br>Coding<br>Circuit Routing Considerations / Thermal<br>Via Keep-Outs<br>Important BTC Package Elements for<br>Physical Design<br>Termination Formats<br>BTC Land Patterns and Component Symbol<br>Coding                                                                                                                                                                                                                                                                 | <ul> <li>50</li> <li>50</li> <li>50</li> <li>51</li> <li>52</li> <li>53</li> <li>53</li> <li>53</li> <li>53</li> </ul>           |
| <ul> <li>6.1</li> <li>6.2</li> <li>6.3</li> <li>6.4</li> <li>6.5</li> <li>6.6</li> <li>6.6.1</li> <li>6.7</li> <li>6.8</li> </ul>                                                                                                                                                        | Design for Assembly Considerations<br>BTC Land Pattern Design Process<br>Package Variations<br>BTC Land Pattern and Component Symbol<br>Coding<br>Circuit Routing Considerations / Thermal<br>Via Keep-Outs<br>Important BTC Package Elements for<br>Physical Design<br>Termination Formats<br>BTC Land Patterns and Component Symbol<br>Coding<br>Solder Mask Design                                                                                                                                                                                                                                           | <ul> <li>50</li> <li>50</li> <li>51</li> <li>52</li> <li>53</li> <li>53</li> <li>53</li> <li>53</li> <li>54</li> </ul>           |
| <ul> <li>6.1</li> <li>6.2</li> <li>6.3</li> <li>6.4</li> <li>6.5</li> <li>6.6</li> <li>6.6.1</li> <li>6.7</li> <li>6.8</li> <li>6.8.1</li> </ul>                                                                                                                                         | Design for Assembly Considerations<br>BTC Land Pattern Design Process<br>Package Variations<br>BTC Land Pattern and Component Symbol<br>Coding<br>Circuit Routing Considerations / Thermal<br>Via Keep-Outs<br>Important BTC Package Elements for<br>Physical Design<br>BTC Land Patterns and Component Symbol<br>Coding<br>Solder Mask Design<br>Encroached Vias                                                                                                                                                                                                                                               | 50<br>50<br>51<br>52<br>53<br>53<br>53<br>53<br>54<br>54                                                                         |
| <ul> <li>6.1</li> <li>6.2</li> <li>6.3</li> <li>6.4</li> <li>6.5</li> <li>6.6</li> <li>6.6.1</li> <li>6.7</li> <li>6.8</li> <li>6.8.1</li> <li>6.8.2</li> </ul>                                                                                                                          | Design for Assembly Considerations<br>BTC Land Pattern Design Process<br>Package Variations<br>BTC Land Pattern and Component Symbol<br>Coding<br>Circuit Routing Considerations / Thermal<br>Via Keep-Outs<br>Important BTC Package Elements for<br>Physical Design<br>Termination Formats<br>BTC Land Patterns and Component Symbol<br>Coding<br>Solder Mask Design<br>Encroached Vias<br>Solder Mask Design for I/O Cu Pads                                                                                                                                                                                  | 50<br>50<br>51<br>52<br>53<br>53<br>53<br>53<br>53<br>54<br>54<br>54                                                             |
| <ul> <li>6.1</li> <li>6.2</li> <li>6.3</li> <li>6.4</li> <li>6.5</li> <li>6.6</li> <li>6.6.1</li> <li>6.7</li> <li>6.8</li> <li>6.8.1</li> <li>6.8.2</li> <li>6.8.3</li> </ul>                                                                                                           | Design for Assembly Considerations<br>BTC Land Pattern Design Process<br>Package Variations<br>BTC Land Pattern and Component Symbol<br>Coding<br>Circuit Routing Considerations / Thermal<br>Via Keep-Outs<br>Important BTC Package Elements for<br>Physical Design<br>BTC Land Patterns and Component Symbol<br>Coding<br>BTC Land Patterns and Component Symbol<br>Coding<br>Solder Mask Design for I/O Cu Pads<br>Solder Mask Design for Thermal Pads                                                                                                                                                       | 50<br>50<br>51<br>52<br>53<br>53<br>53<br>53<br>53<br>54<br>54<br>54<br>54                                                       |
| <ul> <li>6.1</li> <li>6.2</li> <li>6.3</li> <li>6.4</li> <li>6.5</li> <li>6.6</li> <li>6.6.1</li> <li>6.7</li> <li>6.8</li> <li>6.8.1</li> <li>6.8.2</li> <li>6.8.3</li> <li>6.8.4</li> </ul>                                                                                            | Design for Assembly Considerations<br>BTC Land Pattern Design Process<br>Package Variations<br>BTC Land Pattern and Component Symbol<br>Coding<br>Circuit Routing Considerations / Thermal<br>Via Keep-Outs<br>Important BTC Package Elements for<br>Physical Design<br>Termination Formats<br>BTC Land Patterns and Component Symbol<br>Coding<br>Solder Mask Design for I/O Cu Pads<br>Solder Mask Design for Thermal Pads<br>Perimeter I/O Cu Pad Design                                                                                                                                                     | 50<br>50<br>51<br>52<br>53<br>53<br>53<br>53<br>53<br>54<br>54<br>54<br>54<br>54<br>57                                           |
| <ul> <li>6.1</li> <li>6.2</li> <li>6.3</li> <li>6.4</li> <li>6.5</li> <li>6.6</li> <li>6.6.1</li> <li>6.7</li> <li>6.8</li> <li>6.8.1</li> <li>6.8.2</li> <li>6.8.3</li> <li>6.8.4</li> <li>6.9</li> </ul>                                                                               | Design for Assembly Considerations<br>BTC Land Pattern Design Process<br>Package Variations<br>BTC Land Pattern and Component Symbol<br>Coding<br>Circuit Routing Considerations / Thermal<br>Via Keep-Outs<br>Important BTC Package Elements for<br>Physical Design<br>Termination Formats<br>BTC Land Patterns and Component Symbol<br>Coding<br>Solder Mask Design for I/O Cu Pads<br>Solder Mask Design for Thermal Pads<br>Perimeter I/O Cu Pad Design<br>HDI Considerations                                                                                                                               | 50<br>50<br>51<br>52<br>53<br>53<br>53<br>53<br>53<br>54<br>54<br>54<br>54<br>54<br>56<br>57<br>58                               |
| <ul> <li>6.1</li> <li>6.2</li> <li>6.3</li> <li>6.4</li> <li>6.5</li> <li>6.6</li> <li>6.6.1</li> <li>6.7</li> <li>6.8</li> <li>6.8.1</li> <li>6.8.2</li> <li>6.8.3</li> <li>6.8.4</li> <li>6.9</li> <li>6.10</li> </ul>                                                                 | Design for Assembly Considerations<br>BTC Land Pattern Design Process<br>Package Variations<br>BTC Land Pattern and Component Symbol<br>Coding<br>Circuit Routing Considerations / Thermal<br>Via Keep-Outs<br>Important BTC Package Elements for<br>Physical Design<br>Termination Formats<br>BTC Land Patterns and Component Symbol<br>Coding<br>Solder Mask Design for I/O Cu Pads<br>Solder Mask Design for Thermal Pads<br>Perimeter I/O Cu Pad Design<br>HDI Considerations<br>Cu Thermal Pad Design                                                                                                      | 50<br>50<br>51<br>52<br>53<br>53<br>53<br>53<br>53<br>53<br>53<br>54<br>54<br>54<br>54<br>54<br>57<br>58<br>59                   |
| <ul> <li>6.1</li> <li>6.2</li> <li>6.3</li> <li>6.4</li> <li>6.5</li> <li>6.6</li> <li>6.6.1</li> <li>6.7</li> <li>6.8</li> <li>6.8.1</li> <li>6.8.2</li> <li>6.8.3</li> <li>6.8.4</li> <li>6.9</li> <li>6.10</li> <li>6.10.1</li> </ul>                                                 | Design for Assembly Considerations<br>BTC Land Pattern Design Process<br>Package Variations<br>BTC Land Pattern and Component Symbol<br>Coding<br>Circuit Routing Considerations / Thermal<br>Via Keep-Outs<br>Important BTC Package Elements for<br>Physical Design<br>Termination Formats<br>BTC Land Patterns and Component Symbol<br>Coding<br>Solder Mask Design for I/O Cu Pads<br>Solder Mask Design for Thermal Pads<br>Perimeter I/O Cu Pad Design<br>HDI Considerations<br>Cu Thermal Pad Design                                                                                                      | 50<br>50<br>51<br>52<br>53<br>53<br>53<br>53<br>53<br>53<br>53<br>54<br>54<br>54<br>54<br>56<br>57<br>58<br>59<br>59             |
| <ul> <li>6.1</li> <li>6.2</li> <li>6.3</li> <li>6.4</li> <li>6.5</li> <li>6.6</li> <li>6.6.1</li> <li>6.7</li> <li>6.8</li> <li>6.8.1</li> <li>6.8.2</li> <li>6.8.3</li> <li>6.8.4</li> <li>6.9</li> <li>6.10</li> <li>6.10.1</li> <li>6.10.2</li> </ul>                                 | Design for Assembly Considerations<br>BTC Land Pattern Design Process<br>Package Variations<br>BTC Land Pattern and Component Symbol<br>Coding<br>Circuit Routing Considerations / Thermal<br>Via Keep-Outs<br>Important BTC Package Elements for<br>Physical Design<br>Termination Formats<br>BTC Land Patterns and Component Symbol<br>Coding<br>Solder Mask Design for I/O Cu Pads<br>Solder Mask Design for Thermal Pads<br>Perimeter I/O Cu Pad Design<br>HDI Considerations<br>Cu Thermal Pad Design Elements<br>Key Thermal Pad Design Elements                                                          | 50<br>50<br>51<br>52<br>53<br>53<br>53<br>53<br>53<br>53<br>53<br>54<br>54<br>54<br>54<br>54<br>57<br>58<br>59<br>60             |
| <ul> <li>6.1</li> <li>6.2</li> <li>6.3</li> <li>6.4</li> <li>6.5</li> <li>6.6</li> <li>6.6.1</li> <li>6.7</li> <li>6.8</li> <li>6.8.1</li> <li>6.8.2</li> <li>6.8.3</li> <li>6.8.4</li> <li>6.9</li> <li>6.10</li> <li>6.10.1</li> <li>6.10.2</li> <li>6.10.3</li> </ul>                 | Design for Assembly Considerations<br>BTC Land Pattern Design Process<br>Package Variations<br>BTC Land Pattern and Component Symbol<br>Coding<br>Circuit Routing Considerations / Thermal<br>Via Keep-Outs<br>Important BTC Package Elements for<br>Physical Design<br>BTC Land Patterns and Component Symbol<br>Coding<br>Solder Mask Design for I/O Cu Pads<br>Solder Mask Design for I/O Cu Pads<br>Solder Mask Design for Thermal Pads<br>Perimeter I/O Cu Pad Design<br>HDI Considerations<br>Cu Thermal Pad Design Elements<br>Key Thermal Pad Design Elements<br>Thermal Pad Via Counts and Arrangement | 50<br>50<br>51<br>52<br>53<br>53<br>53<br>53<br>53<br>53<br>53<br>54<br>54<br>54<br>54<br>54<br>56<br>57<br>58<br>59<br>60<br>62 |
| <ul> <li>6.1</li> <li>6.2</li> <li>6.3</li> <li>6.4</li> <li>6.5</li> <li>6.6</li> <li>6.6.1</li> <li>6.7</li> <li>6.8</li> <li>6.8.1</li> <li>6.8.2</li> <li>6.8.3</li> <li>6.8.4</li> <li>6.9</li> <li>6.10</li> <li>6.10.1</li> <li>6.10.2</li> <li>6.10.3</li> <li>6.10.4</li> </ul> | Design for Assembly Considerations                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              | 50<br>50<br>51<br>52<br>53<br>53<br>53<br>53<br>53<br>53<br>53<br>53<br>54<br>54<br>54<br>54<br>54<br>57<br>59<br>60<br>62<br>62 |

|                                                                                                                                                                                                                                                                 | Thermal Pad Design Options                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | 64                                                                                                                                                                                                                                             |
|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 6.11.1                                                                                                                                                                                                                                                          | Solder Mask Defined (SMD) Thermal<br>Pad Design                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    | 66                                                                                                                                                                                                                                             |
| 6.11.2                                                                                                                                                                                                                                                          | Open-Cu Thermal Pad Design                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | 69                                                                                                                                                                                                                                             |
| 6.11.3                                                                                                                                                                                                                                                          | Via Tenting Thermal Pad Design                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     | 73                                                                                                                                                                                                                                             |
| 6.11.4                                                                                                                                                                                                                                                          | Encroached Via Thermal Pad Design                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | 74                                                                                                                                                                                                                                             |
| 6.11.5                                                                                                                                                                                                                                                          | Via-in-Pad Plated-Over (VIPPO) Thermal<br>Pad Design                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               | 74                                                                                                                                                                                                                                             |
| 6.11.6                                                                                                                                                                                                                                                          | Floating Mask Thermal Pad Design                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   | 77                                                                                                                                                                                                                                             |
| 6.12                                                                                                                                                                                                                                                            | Design for Cleaning Considerations                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 | 79                                                                                                                                                                                                                                             |
| 6.13                                                                                                                                                                                                                                                            | Stencil Design Considerations                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      | 81                                                                                                                                                                                                                                             |
| 6.13.1                                                                                                                                                                                                                                                          | Thermal Pad Stencil Design                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | 81                                                                                                                                                                                                                                             |
| 6.13.2                                                                                                                                                                                                                                                          | Perimeter I/O Stencil Design                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       | 85                                                                                                                                                                                                                                             |
| 6.14                                                                                                                                                                                                                                                            | Stencil Aperture Reductions                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 85                                                                                                                                                                                                                                             |
| 6.15                                                                                                                                                                                                                                                            | Important Considerations When Designing for BTCs                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   | 86                                                                                                                                                                                                                                             |
| 6.15.1                                                                                                                                                                                                                                                          | Ganged Solder Mask Openings on I/O<br>Terminals                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    | 86                                                                                                                                                                                                                                             |
| 6.15.2                                                                                                                                                                                                                                                          | Mirrored Quad Flat No-Leads (QFN)<br>Constructions                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 | 86                                                                                                                                                                                                                                             |
| 6.16                                                                                                                                                                                                                                                            | Random Via Locations                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               | 86                                                                                                                                                                                                                                             |
| 7 AS<br>B(                                                                                                                                                                                                                                                      | SSEMBLY OF BTCs ON PRINTED<br>OARDS                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                | 86                                                                                                                                                                                                                                             |
| 7.1                                                                                                                                                                                                                                                             | BTC Component Considerations                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       | 86                                                                                                                                                                                                                                             |
| 7.1.1                                                                                                                                                                                                                                                           | Incoming Packaging Formats                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | 87                                                                                                                                                                                                                                             |
| 7.1.2                                                                                                                                                                                                                                                           | BTC Temperature Sensitivity                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | 88                                                                                                                                                                                                                                             |
| 7.1.3                                                                                                                                                                                                                                                           | BTC Moisture Sensitivity Considerations                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            | 00                                                                                                                                                                                                                                             |
|                                                                                                                                                                                                                                                                 | Die Monstare Sensitivity considerations                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            | 00                                                                                                                                                                                                                                             |
| 7.1.4                                                                                                                                                                                                                                                           | BTC Termination Finishes and Solderability                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | 88                                                                                                                                                                                                                                             |
| 7.1.4<br>7.2                                                                                                                                                                                                                                                    | BTC Termination Finishes and Solderability<br>Printed Board Considerations                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | 88<br>89                                                                                                                                                                                                                                       |
| 7.1.4<br>7.2<br>7.2.1                                                                                                                                                                                                                                           | BTC Termination Finishes and Solderability<br>Printed Board Considerations<br>Printed Board Design Impact on Solder<br>Joint Fillet Formation                                                                                                                                                                                                                                                                                                                                                                                                                                      | 88<br>89<br>89                                                                                                                                                                                                                                 |
| <ul><li>7.1.4</li><li>7.2</li><li>7.2.1</li><li>7.2.2</li></ul>                                                                                                                                                                                                 | BTC Termination Finishes and Solderability<br>Printed Board Considerations<br>Printed Board Design Impact on Solder<br>Joint Fillet Formation<br>Assembly-Induced Warpage                                                                                                                                                                                                                                                                                                                                                                                                          | 88<br>89<br>89<br>90                                                                                                                                                                                                                           |
| <ul> <li>7.1.4</li> <li>7.2</li> <li>7.2.1</li> <li>7.2.2</li> <li>7.3</li> </ul>                                                                                                                                                                               | BTC Termination Finishes and Solderability<br>Printed Board Considerations<br>Printed Board Design Impact on Solder<br>Joint Fillet Formation<br>Assembly-Induced Warpage<br>Assembly Materials                                                                                                                                                                                                                                                                                                                                                                                    | 88<br>89<br>89<br>90<br>90                                                                                                                                                                                                                     |
| <ul> <li>7.1.4</li> <li>7.2</li> <li>7.2.1</li> <li>7.2.2</li> <li>7.3</li> <li>7.3.1</li> </ul>                                                                                                                                                                | BTC Termination Finishes and Solderability<br>Printed Board Considerations<br>Printed Board Design Impact on Solder<br>Joint Fillet Formation<br>Assembly-Induced Warpage<br>Solder Alloy Mounting Methods                                                                                                                                                                                                                                                                                                                                                                         | 88<br>89<br>89<br>90<br>90<br>90                                                                                                                                                                                                               |
| <ul> <li>7.1.4</li> <li>7.2</li> <li>7.2.1</li> <li>7.2.2</li> <li>7.3</li> <li>7.3.1</li> <li>7.3.2</li> </ul>                                                                                                                                                 | BTC Termination Finishes and Solderability<br>Printed Board Considerations<br>Printed Board Design Impact on Solder<br>Joint Fillet Formation<br>Assembly-Induced Warpage<br>Assembly Materials<br>Solder Alloy Mounting Methods<br>Solder Paste                                                                                                                                                                                                                                                                                                                                   | 88<br>89<br>89<br>90<br>90<br>90<br>90                                                                                                                                                                                                         |
| <ul> <li>7.1.4</li> <li>7.2</li> <li>7.2.1</li> <li>7.2.2</li> <li>7.3</li> <li>7.3.1</li> <li>7.3.2</li> <li>7.3.3</li> </ul>                                                                                                                                  | BTC Termination Finishes and Solderability<br>Printed Board Considerations<br>Printed Board Design Impact on Solder<br>Joint Fillet Formation<br>Assembly-Induced Warpage<br>Assembly Materials<br>Solder Alloy Mounting Methods<br>Solder Paste<br>Solder Preforms                                                                                                                                                                                                                                                                                                                | 88<br>88<br>89<br>90<br>90<br>90<br>90<br>90                                                                                                                                                                                                   |
| <ul> <li>7.1.4</li> <li>7.2</li> <li>7.2.1</li> <li>7.2.2</li> <li>7.3</li> <li>7.3.1</li> <li>7.3.2</li> <li>7.3.3</li> <li>7.3.4</li> </ul>                                                                                                                   | BTC Termination Finishes and Solderability<br>Printed Board Considerations<br>Printed Board Design Impact on Solder<br>Joint Fillet Formation<br>Assembly-Induced Warpage<br>Assembly Materials<br>Solder Alloy Mounting Methods<br>Solder Paste<br>Solder Preforms<br>Solder Alloy                                                                                                                                                                                                                                                                                                | 88<br>88<br>89<br>90<br>90<br>90<br>90<br>90<br>90                                                                                                                                                                                             |
| <ul> <li>7.1.4</li> <li>7.2</li> <li>7.2.1</li> <li>7.2.2</li> <li>7.3</li> <li>7.3.1</li> <li>7.3.2</li> <li>7.3.3</li> <li>7.3.4</li> <li>7.3.5</li> </ul>                                                                                                    | BTC Termination Finishes and Solderability<br>Printed Board Considerations<br>Printed Board Design Impact on Solder<br>Joint Fillet Formation<br>Assembly-Induced Warpage<br>Assembly Materials<br>Solder Alloy Mounting Methods<br>Solder Paste<br>Solder Preforms<br>Solder Alloy<br>Flux Chemistry Considerations                                                                                                                                                                                                                                                               | <ul> <li>88</li> <li>88</li> <li>89</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>91</li> </ul>                                                                                     |
| 7.1.4<br>7.2<br>7.2.1<br>7.2.2<br>7.3<br>7.3.1<br>7.3.2<br>7.3.3<br>7.3.4<br>7.3.5<br>7.3.6                                                                                                                                                                     | BTC Termination Finishes and Solderability<br>Printed Board Considerations<br>Printed Board Design Impact on Solder<br>Joint Fillet Formation<br>Assembly-Induced Warpage<br>Assembly Materials<br>Solder Alloy Mounting Methods<br>Solder Paste<br>Solder Preforms<br>Solder Alloy<br>Flux Chemistry Considerations<br>Edge/Corner Bond Materials, Underfills and<br>Adhesives                                                                                                                                                                                                    | <ul> <li>88</li> <li>88</li> <li>89</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>91</li> <li>91</li> </ul>                                                                                     |
| <ul> <li>7.1.4</li> <li>7.2</li> <li>7.2.1</li> <li>7.2.2</li> <li>7.3</li> <li>7.3.1</li> <li>7.3.2</li> <li>7.3.3</li> <li>7.3.4</li> <li>7.3.5</li> <li>7.3.6</li> <li>7.4</li> </ul>                                                                        | BTC Termination Finishes and Solderability<br>Printed Board Considerations<br>Printed Board Design Impact on Solder<br>Joint Fillet Formation<br>Assembly-Induced Warpage<br>Assembly Materials<br>Solder Alloy Mounting Methods<br>Solder Paste<br>Solder Preforms<br>Solder Alloy<br>Flux Chemistry Considerations<br>Edge/Corner Bond Materials, Underfills and<br>Adhesives<br>BTC Cleaning Process                                                                                                                                                                            | <ul> <li>88</li> <li>88</li> <li>89</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>91</li> <li>91</li> <li>91</li> </ul>                                                                                     |
| <ul> <li>7.1.4</li> <li>7.2</li> <li>7.2.1</li> <li>7.2.2</li> <li>7.3</li> <li>7.3.1</li> <li>7.3.2</li> <li>7.3.3</li> <li>7.3.4</li> <li>7.3.5</li> <li>7.3.6</li> <li>7.4</li> <li>7.5</li> </ul>                                                           | BTC Termination Finishes and Solderability<br>Printed Board Considerations<br>Printed Board Design Impact on Solder<br>Joint Fillet Formation<br>Assembly-Induced Warpage<br>Assembly Materials<br>Solder Alloy Mounting Methods<br>Solder Paste<br>Solder Preforms<br>Solder Alloy<br>Flux Chemistry Considerations<br>Edge/Corner Bond Materials, Underfills and<br>Adhesives<br>BTC Cleaning Process<br>Solder Printing and Deposition                                                                                                                                          | <ul> <li>88</li> <li>88</li> <li>89</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>91</li> <li>91</li> <li>91</li> <li>92</li> </ul>                                                                         |
| <ul> <li>7.1.4</li> <li>7.2</li> <li>7.2.1</li> <li>7.2.2</li> <li>7.3</li> <li>7.3.1</li> <li>7.3.2</li> <li>7.3.3</li> <li>7.3.4</li> <li>7.3.5</li> <li>7.3.6</li> <li>7.4</li> <li>7.5</li> <li>7.5.1</li> </ul>                                            | BTC Termination Finishes and Solderability<br>Printed Board Considerations<br>Printed Board Design Impact on Solder<br>Joint Fillet Formation<br>Assembly-Induced Warpage<br>Assembly Materials<br>Solder Alloy Mounting Methods<br>Solder Paste<br>Solder Preforms<br>Solder Alloy<br>Flux Chemistry Considerations<br>Edge/Corner Bond Materials, Underfills and<br>Adhesives<br>BTC Cleaning Process<br>Solder Stencil Printing Considerations                                                                                                                                  | <ul> <li>88</li> <li>88</li> <li>89</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>91</li> <li>91</li> <li>91</li> <li>92</li> <li>92</li> </ul>                                                 |
| <ul> <li>7.1.4</li> <li>7.2</li> <li>7.2.1</li> <li>7.2.2</li> <li>7.3</li> <li>7.3.1</li> <li>7.3.2</li> <li>7.3.3</li> <li>7.3.4</li> <li>7.3.5</li> <li>7.3.6</li> <li>7.4</li> <li>7.5</li> <li>7.5.1</li> <li>7.5.2</li> </ul>                             | BTC Termination Finishes and Solderability<br>Printed Board Considerations<br>Printed Board Design Impact on Solder<br>Joint Fillet Formation<br>Assembly-Induced Warpage<br>Assembly Materials<br>Solder Alloy Mounting Methods<br>Solder Paste<br>Solder Preforms<br>Solder Alloy<br>Flux Chemistry Considerations<br>Edge/Corner Bond Materials, Underfills and<br>Adhesives<br>BTC Cleaning Process<br>Solder Stencil Printing Considerations<br>Jetting and Dispensing                                                                                                        | <ul> <li>88</li> <li>89</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>91</li> <li>91</li> <li>91</li> <li>92</li> <li>92</li> <li>93</li> </ul>                                                 |
| <ul> <li>7.1.4</li> <li>7.2</li> <li>7.2.1</li> <li>7.2.2</li> <li>7.3</li> <li>7.3.1</li> <li>7.3.2</li> <li>7.3.3</li> <li>7.3.4</li> <li>7.3.5</li> <li>7.3.6</li> <li>7.4</li> <li>7.5</li> <li>7.5.1</li> <li>7.5.2</li> <li>7.5.3</li> </ul>              | BTC Termination Finishes and Solderability<br>Printed Board Considerations<br>Printed Board Design Impact on Solder<br>Joint Fillet Formation<br>Assembly-Induced Warpage<br>Assembly Materials<br>Solder Alloy Mounting Methods<br>Solder Paste<br>Solder Preforms<br>Solder Preforms<br>Solder Alloy<br>Flux Chemistry Considerations<br>Edge/Corner Bond Materials, Underfills and<br>Adhesives<br>BTC Cleaning Process<br>Solder Stencil Printing Considerations<br>Jetting and Dispensing<br>Solder Dipping of BTC Components                                                 | <ul> <li>88</li> <li>89</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>91</li> <li>91</li> <li>91</li> <li>92</li> <li>92</li> <li>93</li> <li>94</li> </ul>                                     |
| <ul> <li>7.1.4</li> <li>7.2</li> <li>7.2.1</li> <li>7.2.2</li> <li>7.3</li> <li>7.3.1</li> <li>7.3.2</li> <li>7.3.3</li> <li>7.3.4</li> <li>7.3.5</li> <li>7.3.6</li> <li>7.4</li> <li>7.5</li> <li>7.5.1</li> <li>7.5.2</li> <li>7.5.3</li> <li>7.6</li> </ul> | BTC Termination Finishes and Solderability<br>Printed Board Considerations<br>Printed Board Design Impact on Solder<br>Joint Fillet Formation<br>Assembly-Induced Warpage<br>Assembly Materials<br>Solder Alloy Mounting Methods<br>Solder Paste<br>Solder Preforms<br>Solder Preforms<br>Solder Alloy<br>Flux Chemistry Considerations<br>Edge/Corner Bond Materials, Underfills and<br>Adhesives<br>BTC Cleaning Process<br>Solder Printing and Deposition<br>Solder Stencil Printing Considerations<br>Solder Dipping of BTC Components<br>Solder Paste Inspection              | <ul> <li>88</li> <li>88</li> <li>89</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>91</li> <li>91</li> <li>91</li> <li>91</li> <li>91</li> <li>92</li> <li>93</li> <li>94</li> <li>94</li> </ul> |
| 7.1.4<br>7.2<br>7.2.1<br>7.2.2<br>7.3<br>7.3.1<br>7.3.2<br>7.3.3<br>7.3.4<br>7.3.5<br>7.3.6<br>7.4<br>7.5<br>7.5.1<br>7.5.2<br>7.5.3<br>7.6<br>7.7                                                                                                              | BTC Termination Finishes and Solderability<br>Printed Board Considerations<br>Printed Board Design Impact on Solder<br>Joint Fillet Formation<br>Assembly-Induced Warpage<br>Assembly Materials<br>Solder Alloy Mounting Methods<br>Solder Paste<br>Solder Preforms<br>Solder Preforms<br>Solder Alloy<br>Flux Chemistry Considerations<br>Edge/Corner Bond Materials, Underfills and<br>Adhesives<br>BTC Cleaning Process<br>Solder Printing and Deposition<br>Solder Stencil Printing Considerations<br>Jetting and Dispensing<br>Solder Paste Inspection<br>Component Placement | <ul> <li>88</li> <li>89</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>90</li> <li>91</li> <li>91</li> <li>91</li> <li>91</li> <li>92</li> <li>93</li> <li>94</li> <li>94</li> <li>95</li> </ul>             |

| 7.8.1                                                                                                                                                                                                                                  | Reflow Atmosphere                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 7.8.2                                                                                                                                                                                                                                  | Time and Temperature Profiles                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| 7.8.3                                                                                                                                                                                                                                  | Unique Profile for Each Printed Board<br>Assembly                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| 7.8.4                                                                                                                                                                                                                                  | Effects of Materials on Flux Activation,<br>Component Damage and Solderability                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| 7.9                                                                                                                                                                                                                                    | Thermal Profiling for BTCs                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| 7.10                                                                                                                                                                                                                                   | Process Control and Monitoring Points<br>for BTCs                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| 7.11                                                                                                                                                                                                                                   | Production-Level Inspection                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| 7.11.1                                                                                                                                                                                                                                 | Automated Solder Paste Inspection (SPI) 99                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| 7.11.2                                                                                                                                                                                                                                 | Automated Optical Inspection (AOI) 100                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| 7.11.3                                                                                                                                                                                                                                 | Automated X-Ray Inspection (AXI) 102                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| 7.11.4                                                                                                                                                                                                                                 | Manual Visual Inspection 103                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| 7.12                                                                                                                                                                                                                                   | Rework and Repair 104                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| 7.12.1                                                                                                                                                                                                                                 | Hot-Air Rework 104                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| 7.12.2                                                                                                                                                                                                                                 | Alternative Methods 104                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| 7.12.3                                                                                                                                                                                                                                 | I/O Lead Manual Touch-Up 105                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| 7.13                                                                                                                                                                                                                                   | Conformal Coating of BTCs 105                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| 7.14                                                                                                                                                                                                                                   | Mechanical Heat Sink Usage 105                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |
| 7.15                                                                                                                                                                                                                                   | Production-Level Testing 106                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| 7.15.1                                                                                                                                                                                                                                 | Electrical Testing 106                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| 7.15.2                                                                                                                                                                                                                                 | Functional Test (FT) Coverage 106                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| 7 15 3                                                                                                                                                                                                                                 | Dum In Testing 106                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| 1.13.3                                                                                                                                                                                                                                 | Burn-In Testing 100                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| 7.15.4                                                                                                                                                                                                                                 | Product Screening Tests 106                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| 7.15.4<br>8 RI<br>F(                                                                                                                                                                                                                   | Product Screening Tests                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| 7.15.3<br>7.15.4<br>8 Ri<br>F(<br>8.1                                                                                                                                                                                                  | Burn-Int Testing       100         Product Screening Tests       106         ELIABILITY CONSIDERATIONS       106         Introduction and Reliability Fundamentals       106         Introduction and Reliability Fundamentals       106                                                                                                                                                                                                                                                                                                                                                                                                                                   |
| 7.15.3<br>7.15.4<br>8 Ri<br>F(<br>8.1<br>8.1.1                                                                                                                                                                                         | Burn-Ini Testing       106         Product Screening Tests       106         ELIABILITY CONSIDERATIONS       106         Introduction and Reliability Fundamentals of BTCs       106         Mechanical Reliability       107                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| 7.15.3<br>7.15.4<br>8 RI<br>FC<br>8.1<br>8.1.1<br>8.1.1<br>8.1.2                                                                                                                                                                       | Burn-Int Testing       106         Product Screening Tests       106         ELIABILITY CONSIDERATIONS       106         Introduction and Reliability Fundamentals of BTCs       106         Mechanical Reliability       107         Thermomechanical Reliability       107                                                                                                                                                                                                                                                                                                                                                                                               |
| 7.15.3<br>7.15.4<br>8 RI<br>FC<br>8.1<br>8.1.1<br>8.1.1<br>8.1.2<br>8.1.3                                                                                                                                                              | Product Screening Tests       106         ELIABILITY CONSIDERATIONS       106         Introduction and Reliability Fundamentals of BTCs       106         Mechanical Reliability       107         Thermomechanical Reliability       107         Electrochemical Reliability       107                                                                                                                                                                                                                                                                                                                                                                                    |
| 7.15.3<br>7.15.4<br>8 Ri<br>FC<br>8.1<br>8.1.1<br>8.1.1<br>8.1.2<br>8.1.3<br>8.2                                                                                                                                                       | Burn-Ini Testing       100         Product Screening Tests       106         ELIABILITY CONSIDERATIONS       106         Introduction and Reliability Fundamentals of BTCs       106         Mechanical Reliability       107         Thermomechanical Reliability       107         Electrochemical Reliability       107         Designing for BTC Reliability –       107         Thermomechanical Loads       108                                                                                                                                                                                                                                                      |
| 7.15.3<br>7.15.4<br>8 RI<br>FC<br>8.1<br>8.1.1<br>8.1.2<br>8.1.3<br>8.2<br>8.2.1                                                                                                                                                       | Burn-III Testing       100         Product Screening Tests       106         ELIABILITY CONSIDERATIONS       106         Introduction and Reliability Fundamentals       106         Mechanical Reliability       107         Thermomechanical Reliability       107         Electrochemical Reliability       107         Designing for BTC Reliability       107         Premomechanical Loads       108         Printed Board Design Considerations       108                                                                                                                                                                                                           |
| 7.15.3<br>7.15.4<br>8 RI<br>FC<br>8.1<br>8.1.1<br>8.1.2<br>8.1.3<br>8.2<br>8.2.1<br>8.2.2                                                                                                                                              | Burn-III Testing100Product Screening Tests106ELIABILITY CONSIDERATIONS<br>DR BTCs106Introduction and Reliability Fundamentals<br>of BTCs106Mechanical Reliability107Thermomechanical Reliability107Electrochemical Reliability107Designing for BTC Reliability107Designing for BTC Reliability108Printed Board Design Considerations108Printed Board Thickness108                                                                                                                                                                                                                                                                                                          |
| 7.15.3<br>7.15.4<br>8 RI<br>8.1<br>8.1.1<br>8.1.1<br>8.1.2<br>8.1.3<br>8.2<br>8.2.1<br>8.2.2<br>8.2.3                                                                                                                                  | Burn-III Testing       100         Product Screening Tests       106         ELIABILITY CONSIDERATIONS       106         Introduction and Reliability Fundamentals of BTCs       106         Mechanical Reliability       107         Thermomechanical Reliability       107         Electrochemical Reliability       107         Designing for BTC Reliability       107         Printed Board Design Considerations       108         Printed Board Thickness       108         Wettable Side Flank Effect on Reliability       108                                                                                                                                     |
| 7.15.3<br>7.15.4<br>8 RI<br>FC<br>8.1<br>8.1.1<br>8.1.2<br>8.1.3<br>8.2<br>8.2.1<br>8.2.2<br>8.2.3<br>8.2.4                                                                                                                            | Burn-Ini Testing       106         Product Screening Tests       106         ELIABILITY CONSIDERATIONS       106         Introduction and Reliability Fundamentals of BTCs       106         Mechanical Reliability       107         Thermomechanical Reliability       107         Electrochemical Reliability       107         Designing for BTC Reliability –       108         Printed Board Design Considerations       108         Printed Board Thickness       108         Wettable Side Flank Effect on Reliability       108         Land Size and Pitch       109                                                                                             |
| 7.15.3<br>7.15.4<br>8 RI<br>8.1<br>8.1.1<br>8.1.2<br>8.1.3<br>8.2<br>8.2.1<br>8.2.2<br>8.2.3<br>8.2.4<br>8.2.5                                                                                                                         | Burn-III Testing       100         Product Screening Tests       106         ELIABILITY CONSIDERATIONS       106         Introduction and Reliability Fundamentals of BTCs       106         Mechanical Reliability       107         Thermomechanical Reliability       107         Electrochemical Reliability       107         Designing for BTC Reliability       107         Designing for BTC Reliability       108         Printed Board Design Considerations       108         Printed Board Thickness       108         Wettable Side Flank Effect on Reliability       109         BTC Package Stand-Off       109                                             |
| 7.15.3<br>7.15.4<br>8 RI<br>FC<br>8.1<br>8.1.1<br>8.1.2<br>8.1.3<br>8.2<br>8.2.1<br>8.2.2<br>8.2.3<br>8.2.4<br>8.2.5<br>8.2.6                                                                                                          | Burn-Ini Testing       100         Product Screening Tests       106         ELIABILITY CONSIDERATIONS       106         Introduction and Reliability Fundamentals of BTCs       106         Mechanical Reliability       107         Thermomechanical Reliability       107         Electrochemical Reliability       107         Designing for BTC Reliability       107         Designing for BTC Reliability       108         Printed Board Design Considerations       108         Printed Board Thickness       108         Wettable Side Flank Effect on Reliability       109         BTC Package Stand-Off       109         Benefits of Reinforcement       110 |
| 7.15.3<br>7.15.4<br>8 RI<br>8.1<br>8.1.1<br>8.1.2<br>8.1.3<br>8.2<br>8.2.1<br>8.2.2<br>8.2.3<br>8.2.4<br>8.2.5<br>8.2.6<br>8.2.7                                                                                                       | Burn-Int Testing       100         Product Screening Tests       106         ELIABILITY CONSIDERATIONS       106         Introduction and Reliability Fundamentals of BTCs       106         Mechanical Reliability       107         Thermomechanical Reliability       107         Electrochemical Reliability       107         Designing for BTC Reliability       107         Designing for BTC Reliability       108         Printed Board Design Considerations       108         Printed Board Thickness       108         Wettable Side Flank Effect on Reliability       109         BTC Package Stand-Off       109         Benefits of Reinforcement       110 |
| 7.15.3<br>7.15.4<br>8 RI<br>FC<br>8.1<br>8.1.1<br>8.1.2<br>8.1.3<br>8.2<br>8.2.1<br>8.2.2<br>8.2.3<br>8.2.4<br>8.2.5<br>8.2.6<br>8.2.7<br>8.2.8                                                                                        | Burn-Int Testing       100         Product Screening Tests       106         ELIABILITY CONSIDERATIONS       106         Introduction and Reliability Fundamentals of BTCs       106         Mechanical Reliability       107         Thermomechanical Reliability       107         Electrochemical Reliability       107         Designing for BTC Reliability –       108         Printed Board Design Considerations       108         Printed Board Thickness       108         Wettable Side Flank Effect on Reliability       109         BTC Package Stand-Off       109         Benefits of Reinforcement       110         Mold Compound Material       110      |
| 7.15.3<br>7.15.4<br>8 RI<br>8.1<br>8.1<br>8.1.1<br>8.1.2<br>8.1.3<br>8.2<br>8.2.1<br>8.2.2<br>8.2.3<br>8.2.4<br>8.2.5<br>8.2.6<br>8.2.7<br>8.2.8<br>8.2.9                                                                              | Burn-III Testing100Product Screening Tests106ELIABILITY CONSIDERATIONS<br>OR BTCs106Introduction and Reliability Fundamentals<br>of BTCs106Mechanical Reliability107Thermomechanical Reliability107Electrochemical Reliability107Designing for BTC Reliability107Designing for BTC Reliability108Printed Board Design Considerations108Printed Board Thickness108Wettable Side Flank Effect on Reliability109BTC Package Stand-Off109Benefits of Reinforcement110Die Size110Solder Joint Microstructure Embrittlement110                                                                                                                                                   |
| 7.15.3         7.15.4         8       Ri         8.1       8.1.1         8.1.2       8.1.3         8.2       8.2.1         8.2.2       8.2.3         8.2.4       8.2.5         8.2.6       8.2.7         8.2.8       8.2.9         8.3 | Burn-Int Testing100Product Screening Tests106ELIABILITY CONSIDERATIONS<br>OR BTCs106Introduction and Reliability Fundamentals<br>of BTCs106Mechanical Reliability107Thermomechanical Reliability107Electrochemical Reliability107Designing for BTC Reliability –<br>Thermomechanical Loads108Printed Board Design Considerations108Printed Board Thickness108Wettable Side Flank Effect on Reliability109BTC Package Stand-Off109Benefits of Reinforcement110Mold Compound Material110Die Size110Solder Joint Microstructure Embrittlement110Designing for BTC Reliability – Electrical<br>and Thermal Loads110                                                            |
| 7.15.3<br>7.15.4<br>8 RI<br>8.1<br>8.1<br>8.1.1<br>8.1.2<br>8.1.3<br>8.2<br>8.2.1<br>8.2.2<br>8.2.3<br>8.2.4<br>8.2.5<br>8.2.6<br>8.2.7<br>8.2.8<br>8.2.9<br>8.3<br>8.3.1                                                              | Burn-III Testing100Product Screening Tests106ELIABILITY CONSIDERATIONS<br>OR BTCs106Introduction and Reliability Fundamentals<br>of BTCs106Mechanical Reliability107Thermomechanical Reliability107Electrochemical Reliability107Designing for BTC Reliability107Designing for BTC Reliability108Printed Board Design Considerations108Printed Board Thickness108Wettable Side Flank Effect on Reliability109BTC Package Stand-Off109Benefits of Reinforcement110Die Size110Solder Joint Microstructure Embrittlement110Voids in Thermal Pad110                                                                                                                            |
| 7.15.3<br>7.15.4<br>8 RI<br>8.1<br>8.1.1<br>8.1.2<br>8.1.3<br>8.2<br>8.2.1<br>8.2.2<br>8.2.3<br>8.2.4<br>8.2.5<br>8.2.6<br>8.2.7<br>8.2.8<br>8.2.9<br>8.3<br>8.3.1<br>8.4                                                              | Burn-Int Testing100Product Screening Tests106ELIABILITY CONSIDERATIONS<br>OR BTCs106Introduction and Reliability Fundamentals<br>of BTCs106Mechanical Reliability107Thermomechanical Reliability107Electrochemical Reliability107Designing for BTC Reliability –<br>Thermomechanical Loads108Printed Board Design Considerations108Printed Board Thickness108Wettable Side Flank Effect on Reliability109BTC Package Stand-Off109Benefits of Reinforcement110Mold Compound Material110Die Size110Solder Joint Microstructure Embrittlement110Voids in Thermal Pad110Reliability Testing111                                                                                 |

| 8.4.1 | Time Zero Assembly Analysis by<br>Endoscopy 111                     |
|-------|---------------------------------------------------------------------|
| 8.4.2 | Accelerated Life Testing for<br>Thermomechanical Reliability 112    |
| 8.4.3 | Accelerated Lifetime Testing for<br>Reliability Under Damp Heat 113 |
| 9 TF  | ROUBLESHOOTING 113                                                  |
| 9.1   | Assembly and Fabrication Issues 113                                 |
| 9.1.1 | Printed Board Supplier Solder Mask<br>Alterations 113               |
| 9.1.2 | SMT Stencil Alterations 114                                         |
| 9.1.3 | Alternate SMT Stencil Printing Methods 114                          |
| 9.1.4 | Thermal Pad Voiding 114                                             |
| 9.1.5 | I/O Pad Voiding 114                                                 |
| 9.1.6 | I/O Opens and Bridging 114                                          |
| 9.2   | BTC Assembly Troubleshooting 115                                    |
| 9.2.1 | BTC Open Joints 115                                                 |
| 9.2.2 | Solder Attachment Failures 115                                      |
| APPEN | IDIX A Index of Acronyms and<br>Abbreviations                       |

### Figures

| Figure 1-1  | Various Forms of BTCs 1                                                                                                                                         |
|-------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Figure 3-1  | Bottom Side of a Flip Chip QFN (FC-QFN)<br>Showing Irregular Solder Terminations                                                                                |
| Figure 3-2  | BTC Packages without Thermal Pads<br>(Top and Bottom View)                                                                                                      |
| Figure 3-3  | Single Thermal Pad QFN Device Example<br>(Top and Bottom View)                                                                                                  |
| Figure 3-4  | Multiple Thermal Pad Design Example7                                                                                                                            |
| Figure 3-5  | Saw-Singulated BTC Packages 8                                                                                                                                   |
| Figure 3-6  | Package Thickness Comparison 8                                                                                                                                  |
| Figure 3-7  | Example of a Printed Board Land Pattern with a Single Thermal Pad Containing Vias 10                                                                            |
| Figure 3-8  | Examples of Printed Board Land Patterns with Multiple Thermal Pads 11                                                                                           |
| Figure 3-9  | Comparison of a Standard BTC Termination and a Wettable Side Flank BTC Termination . 11                                                                         |
| Figure 3-10 | Step-Cut Quad Flat No-Lead (QFN)<br>Package Assembly Process                                                                                                    |
| Figure 3-11 | Sn-Plated Step-Cut Quad Flat No-Lead<br>(QFN) Lead Frame12                                                                                                      |
| Figure 3-12 | Concave Depression Etched into the<br>Bottom Surface of the Contact                                                                                             |
| Figure 3-13 | Cross-Sectional Representations of Wettable<br>Flank Terminations for Mechanical Precut<br>Process (Left) and Dimple-Formation<br>Chemical Etch Process (Right) |
| Figure 3-14 | Microscopic Images of Common Pin<br>Modifications                                                                                                               |
| Figure 3-15 | Transparent View of a DFN Package 14                                                                                                                            |
|             |                                                                                                                                                                 |

| Figure 3-16 | Detailed View on Side Wettable Flank                                                                                  | 11 |
|-------------|-----------------------------------------------------------------------------------------------------------------------|----|
| Figuro 4-1  | Various Forms of PTC Parts                                                                                            | 14 |
| Figure 4-1  | Singulated Land Grid Array (LGA)                                                                                      | 15 |
| Figure 4-2  | (Bottom of Device)                                                                                                    | 15 |
| Figure 4-3  | Basic Single-Row Lead-Frame-Based Small<br>Outline No-Lead (SON)-Quad Flat No-Lead<br>(QFN) Package Assembly Model    | 16 |
| Figure 4-4  | Basic Multiple-Row Quad Flat No-Lead (QFN) Package Assembly Model                                                     | 16 |
| Figure 4-5  | Terminal Configuration for Single-Row<br>Small Outline No-Lead (SON) and Quad<br>Flat No-Lead (QFN) Packaging         | 16 |
| Figure 4-6  | JEDEC-Defined Package Outlines for<br>Single-Row Small Outline No-Lead (SON)<br>and Quad-Flat No-Lead (QFN) Packaging | 17 |
| Figure 4-7  | Terminal Design Variations for Single-Row<br>Small Outline No-Lead (SON) and Quad<br>Flat No-Lead (QFN) Packaging     | 18 |
| Figure 4-8  | Terminal Contact Layout                                                                                               | 19 |
| Figure 4-9  | Depopulation Schemes for Single-Row                                                                                   | 10 |
| Figure 4-10 | Corner Terminals and Exposed Heat                                                                                     | 13 |
|             | Spreader                                                                                                              | 19 |
| Figure 4-11 | Metallized Terminal                                                                                                   | 20 |
| Figure 4-12 | (QFN) Packaging                                                                                                       | 20 |
| Figure 4-13 | Quad Flat No-Lead (QFN) Dual-Row<br>Package (Top and Side Views)                                                      | 21 |
| Figure 4-14 | Outer and Inner Terminal Layout Variations                                                                            | 22 |
| Figure 4-15 | Two-Row Terminal Layout                                                                                               | 22 |
| Figure 4-16 | Notch Feature on Exposed Die-Attach Pad                                                                               | 23 |
| Figure 4-17 | Two- and Three-Row Quad Flat No-Lead (QFN) Package Examples                                                           | 23 |
| Figure 4-18 | Basic Two-Row Terminal Layout Variations                                                                              | 24 |
| Figure 4-19 | Basic Three-Row Terminal Layout<br>Variations                                                                         | 24 |
| Figure 4-20 | Contact Geometry Variations                                                                                           | 25 |
| Figure 4-21 | Basic Quad Flat No-Lead (QFN) Package<br>Outline Drawing                                                              | 25 |
| Figure 4-22 | Pin 1 Location Option                                                                                                 | 26 |
| Figure 4-23 | Various BTC Packages                                                                                                  | 26 |
| Figure 4-24 | Typical Die Attach Side Lead Frame with NiPdAu Finish for QFNs                                                        | 27 |
| Figure 4-25 | Typical Solder Pad Side of a Quad Flat<br>No-Lead (QFN) Panel with Tape Over<br>the Lead Frame                        | 27 |
| Figure 4-26 | Quad Flat No-Lead (QFN) Fabrication with Saw Singulation                                                              | 28 |
| Figure 4-27 | Over-Molded Lead Frame Configuration                                                                                  | 28 |
| Figure 4-28 | Quad Flat No-Lead (QFN) Fabrication<br>with Punch Singulation                                                         | 29 |
| Figure 4-29 | Comparing Punch-Singulated and<br>Saw-Singulated Packages with Wire<br>Bond Options                                   | 29 |
|             |                                                                                                                       |    |

| Figure 4-30 | <ul> <li>Example of Half-Etch Pull-Back Contact<br/>and Full-Etch No-Pull-Back Perimeter<br/>Contact Configurations</li></ul>           |
|-------------|-----------------------------------------------------------------------------------------------------------------------------------------|
| Figure 4-31 | Plating Layer Construction Comparison 32                                                                                                |
| Figure 4-32 | 2 Detailed View of a Custom Site for a QFN 33                                                                                           |
| Figure 4-33 | Example 28 I/O Micro Lead Frame (MLF)<br>Package                                                                                        |
| Figure 4-34 | Micro Lead Package (MLP) 33                                                                                                             |
| Figure 4-35 | Micro Lead Frame Plastic Package (MLFP) 34                                                                                              |
| Figure 4-36 | JEDEC MO-220 Package Outline 34                                                                                                         |
| Figure 4-37 | QFN Contact Design 35                                                                                                                   |
| Figure 4-38 | Lead Frame Chip Scale Package (LFCSP) 36                                                                                                |
| Figure 4-39 | Leadless Lead Frame Package (LLP)                                                                                                       |
| Figure 4-40 | <ul> <li>Typical Leadless Lead Frame Package</li> <li>(LLP) and Lead Frame Chip Scale</li> <li>Package (LFCSP) Outline Detail</li></ul> |
| Figure 4-41 | Examples of Land Grid Array (LGA)<br>Packages                                                                                           |
| Figure 4-42 | Air Cavity and Over-Molded Land Grid<br>Array (LGA) Constructions                                                                       |
| Figure 4-43 | Sample Ball Grid Array (BGA) Process<br>Flow                                                                                            |
| Figure 4-44 | Sample Molded Land Grid Array (LGA)<br>Process Flow 40                                                                                  |
| Figure 4-45 | Sample Air Cavity Land Grid Array (LGA)<br>Process Flow 40                                                                              |
| Figure 4-46 | Top (Primary) View of Land Grid Array<br>(LGA) Printed Board 41                                                                         |
| Figure 4-47 | Bottom (Secondary) View of Land Grid<br>Array (LGA) Printed Board 41                                                                    |
| Figure 5-1  | Cross-Section of HASL-Coated Lead;<br>Not Flat                                                                                          |
| Figure 5-2  | Nonuniform HASL Pads (Left) and Coordinate<br>Measuring Machine (CMM) Image Showing<br>HASL Coating Variation (Right)                   |
| Figure 5-3  | Solid Solder Deposition (SSD) Application<br>Basic Fabrication Steps                                                                    |
| Figure 5-4  | SSD Process Steps 49                                                                                                                    |
| Figure 5-5  | Silk Screen Thickness Measurement on Solder Mask 49                                                                                     |
| Figure 6-1  | Quad-Flat No-Lead (QFN) / Small-<br>Outline No-Lead (SON) Family of<br>Bottom Termination Components (BTC) 51                           |
| Figure 6-2  | BTC Printed Board Land Pattern<br>Dimensions                                                                                            |
| Figure 6-3  | Comparing Pull-Back (Left) and No<br>Pull-Back Configurations (Right)                                                                   |
| Figure 6-4  | Bottom (Left) and Inclined (Right)<br>Microscopic Images of Pull-Back Leads<br>on a Quad Flat No-Lead (QFN) Package 53                  |
| Figure 6-5  | Cross-Sectional Microscopic Image of<br>Pull-Back                                                                                       |
| Figure 6-6  | Comparing Solder Mask Off Via Land with a Solder Mask Encroached Via Land                                                               |
| Figure 6-7  | Comparing Optional Solder Mask<br>Variations                                                                                            |

#### October 2020

| Figure 6-8  | NSMD I/O Design Example 55                                                                                                |
|-------------|---------------------------------------------------------------------------------------------------------------------------|
| Figure 6-9  | Singulated and Ganged I/O Terminations 55                                                                                 |
| Figure 6-10 | Ganged I/O Terminations with Solder Mask<br>Opening                                                                       |
| Figure 6-11 | Surface Power/Ground Flooding into QFN<br>Device                                                                          |
| Figure 6-12 | Printed Board Supplier Removal of<br>Solder Mask Web                                                                      |
| Figure 6-13 | Solder Mask Defined (SMD) (Left) and<br>Non-Solder Mask Defined (NSMD) (Right)<br>Exposed-Pad Soldering Design Options 56 |
| Figure 6-14 | Exposed-Pad Soldering Area Singulated by<br>Solder Mask Web                                                               |
| Figure 6-15 | Bulbus Solder Joint Formation (Left) Pin<br>Protrudes (Right) Beyond Package Body<br>Edge                                 |
| Figure 6-16 | Pad Larger Than Package Pin (Left)<br>and Pin Pulled Back from Package<br>Body Edge (Right)                               |
| Figure 6-17 | Concave Solder Joint Formation (Left)<br>and Pin Protrudes Beyond Package<br>Body Edge (Right)                            |
| Figure 6-18 | Pad Same Size as Package Pin (Left)<br>and Pin Pulled Back from Package<br>Body Edge (Right)                              |
| Figure 6-19 | HDI Build-Ups Possible with Laser-Via<br>Generation                                                                       |
| Figure 6-20 | HDI Build-Ups Possible with Etching and<br>Mechanical Processes                                                           |
| Figure 6-21 | Planarized and Capped Via Protection<br>Example                                                                           |
| Figure 6-22 | Via Protection Methods                                                                                                    |
| Figure 6-23 | Cu Thermal Pad Design Showing Thermal<br>Via and Thermal Paddle                                                           |
| Figure 6-24 | Printed Board Mean Thermal Resistance of<br>an Array of Vias with a Given Via Density 62                                  |
| Figure 6-25 | Orthogonal Array Structure (Left) and<br>Hexagonal Array Structure (Right)                                                |
| Figure 6-26 | Random Thermal Via Placement Example –<br>Not Recommended                                                                 |
| Figure 6-27 | Inadequate Via Quantity 64                                                                                                |
| Figure 6-28 | Heat Transfer Using Printed Board<br>Thermal Pad64                                                                        |
| Figure 6-29 | BTC Thermal Pad Design Options 65                                                                                         |
| Figure 6-30 | Solder Mask Design (SMD) 66                                                                                               |
| Figure 6-31 | Common Solder Mask Defined (SMD)<br>Layouts for Fewer Than Nine Vias                                                      |
| Figure 6-32 | Common Solder Mask Defined (SMD)<br>Layouts Incorporating Nine Vias                                                       |
| Figure 6-33 | Complex Solder Mask Defined (SMD)<br>Layouts with Via Counts Ranging from<br>Five to 31 or More                           |
| Figure 6-34 | Multiple Thermal Pad Design Example 68                                                                                    |
| Figure 6-35 | Open Thermal Through-Hole Via Structure 70                                                                                |
| Figure 6-36 | Solder Protrusion Under Quad Flat No-<br>Lead (QFN) Thermal Pad70                                                         |
| Figure 6-37 | Graph of Soldered Area and Filling of Vias 71                                                                             |

| Figure 6-38 | Process Variation (Blue Gaussian Curve)<br>Results in Filled Vias if the Nominal Solder<br>Volume Is Set to V <sub>max</sub>                                          | 72 |
|-------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------|----|
| Figure 6-39 | Micrograph of Quad Flat No-Lead (QFN)<br>Land with Open-Cu Thermal Pad Design<br>(Left), Solder Print Window Pattern (Middle)<br>and X-Ray Image After Reflow (Right) | 72 |
| Figure 6-40 | Open-Cu Via Design without Solder Mask<br>(Left) and with Encroached Solder Mask<br>(Right) on the Bottom Side of the Printed<br>Board                                | 72 |
| Figure 6-41 | Design with Top-Side Solder Mask Via<br>Tenting                                                                                                                       | 73 |
| Figure 6-42 | Via-in-Pad Plated-Over (VIPPO) Thermal<br>Pad Illustration and Cross-Section                                                                                          | 75 |
| Figure 6-43 | Via-in-Pad Plated-Over (VIPPO) Pad<br>Example                                                                                                                         | 76 |
| Figure 6-44 | Floating Solder Mask Via Design<br>Cross-Sectional and Planar Top Views                                                                                               | 77 |
| Figure 6-45 | Singulated Mask Location vs. Tied Mask Location                                                                                                                       | 78 |
| Figure 6-46 | Example Implementation of a Floating Solder Mask Design                                                                                                               | 78 |
| Figure 6-47 | Cu Area Printed with Single Bulk Aperture<br>(Left) and Cu Area Printed with Multiple<br>Apertures (Right)                                                            | 81 |
| Figure 6-48 | Voiding Levels Using Single Bulk Apertures<br>(Left) and Voiding Levels Using Striped<br>Apertures (Right)                                                            | 82 |
| Figure 6-49 | Optimized Striped and Bulk Thermal Pad<br>Stencil Aperture Designs Based on Large<br>Body (Top) and Small Body (Bottom)<br>Package Sizes                              | 83 |
| Figure 6-50 | Example of Thermal Pad Basis Percentage<br>Coverage                                                                                                                   | 84 |
| Figure 6-51 | Solder Aperture Basis Percentage                                                                                                                                      | 85 |
| Figure 6-52 | 1:1 Stencil Aperture Beductions                                                                                                                                       | 85 |
| Figure 6-53 | Mirrored Quad Flat No-Lead (QFN)                                                                                                                                      | 00 |
|             | Example Structure                                                                                                                                                     | 86 |
| Figure 7-1  | JEDEC Tray Carrier Format                                                                                                                                             | 87 |
| Figure 7-2  | Elevated Process Windows Increasing<br>Temperature-Sensitive Component<br>(TSC) Risks                                                                                 | 88 |
| Figure 7-3  | Surface Defects Caused by Probe Marks                                                                                                                                 | 89 |
| Figure 7-4  | Resultant Good (Left) vs. Bad (Right)<br>Solder Joint Formation for Pull-Back<br>and No Pull-Back BTC Configurations                                                  | 89 |
| Figure 7-5  | BTC with Outrigger Pads                                                                                                                                               | 90 |
| Figure 7-6  | Solder Preform in Tape-and-Reel Format                                                                                                                                | 90 |
| Figure 7-7  | Flux Residue Entrapment Area of Concern                                                                                                                               | 91 |
| Figure 7-8  | Flux Residue Deposits Under a Dual-Row<br>QFN                                                                                                                         | 91 |
| Figure 7-9  | Recommended Aperture Dimensions for<br>Commonly Used Stencil Thicknesses                                                                                              | 93 |
| Figure 7-10 | Comparison of Solder-Dipped and Not<br>Dipped BTCs and Resultant No Solder<br>Condition                                                                               | 94 |

| Figure 7-11                                                                                                                                                                        | Assessing Maximum Off-Land Acceptance<br>(Before Reflow)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Figure 7-12                                                                                                                                                                        | Metal-Defined Land Solder Joint 96                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| Figure 7-13                                                                                                                                                                        | Profile for SnPb Solder Reflow                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| Figure 7-14                                                                                                                                                                        | Profile for SAC Alloy Solder Reflow                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| Figure 7-15                                                                                                                                                                        | BTC Thermal Pad Thermocouple Location<br>for Profile Verification (Red Marker)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               |
| Figure 7-16                                                                                                                                                                        | Example Solder Paste Inspection Image of a Quad Flat No-Lead (QFN)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |
| Figure 7-17                                                                                                                                                                        | Microscopy Images of Wetted (Top, Left)<br>and Nonwetted (Top, Right) Terminations<br>of a MLF Package                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| Figure 7-18                                                                                                                                                                        | Impact of Wetting Height 101                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| Figure 7-19                                                                                                                                                                        | Good vs. Bad Wetted Pin (Side-Flank)<br>Detection Using AOI 101                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| Figure 7-20                                                                                                                                                                        | Quad Flat No-Lead (QFN) with Wettable<br>Flanks 102                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| Figure 7-21                                                                                                                                                                        | Cross-Section of Quad Flat No-Lead<br>(QFN) with Wettable Flanks 102                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| Figure 7-22                                                                                                                                                                        | X-Ray Images Using Various Techniques<br>to Detect Missing Solder                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| Figure 7-23                                                                                                                                                                        | Example BTC X-Ray Images Using<br>Automated X-Ray Inspection (AXI)<br>After Assembly                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |
| Figure 7-24                                                                                                                                                                        | Solder Bumping Method Using a Stencil 104                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| Figure 8-1                                                                                                                                                                         | Solder Crack in an I/O BTC Solder Joint                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
|                                                                                                                                                                                    | (CTE) Mismatch After 1,000 Cycles 107                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| Figure 8-2                                                                                                                                                                         | (CTE) Mismatch After 1,000 Cycles                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| Figure 8-2<br>Figure 8-3                                                                                                                                                           | Due to Coefficient of Thermal Expansion         (CTE) Mismatch After 1,000 Cycles                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |
| Figure 8-2<br>Figure 8-3<br>Figure 8-4                                                                                                                                             | Due to Coefficient of Thermal Expansion<br>(CTE) Mismatch After 1,000 Cycles       107         Weibull Plot Demonstrating Thinner Printed<br>Boards Result in Higher Fatigue Life       108         Land Size Effect on Fatigue Life of 7-mm<br>BTC Packages       109         Plate-Up Bumped Option       109                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |
| Figure 8-2<br>Figure 8-3<br>Figure 8-4<br>Figure 8-5                                                                                                                               | Ute to Coefficient of Thermal Expansion         (CTE) Mismatch After 1,000 Cycles         Weibull Plot Demonstrating Thinner Printed         Boards Result in Higher Fatigue Life         Land Size Effect on Fatigue Life of 7-mm         BTC Packages       109         Plate-Up Bumped Option       109         Stand-Off Effect on Fatigue Life of a BTC       109                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
| Figure 8-2<br>Figure 8-3<br>Figure 8-4<br>Figure 8-5<br>Figure 8-6                                                                                                                 | Due to Coefficient of Thermal Expansion<br>(CTE) Mismatch After 1,000 Cycles       107         Weibull Plot Demonstrating Thinner Printed<br>Boards Result in Higher Fatigue Life       108         Land Size Effect on Fatigue Life of 7-mm<br>BTC Packages       109         Plate-Up Bumped Option       109         Stand-Off Effect on Fatigue Life of a BTC       109         Heat Transfer with Exposed Pad Solder<br>Joints       110                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| Figure 8-2<br>Figure 8-3<br>Figure 8-4<br>Figure 8-5<br>Figure 8-6<br>Figure 8-7                                                                                                   | Due to Coefficient of Thermal Expansion<br>(CTE) Mismatch After 1,000 Cycles       107         Weibull Plot Demonstrating Thinner Printed<br>Boards Result in Higher Fatigue Life       108         Land Size Effect on Fatigue Life of 7-mm<br>BTC Packages       109         Plate-Up Bumped Option       109         Stand-Off Effect on Fatigue Life of a BTC       109         Heat Transfer with Exposed Pad Solder<br>Joints       110         Calculation of Void Influence Within<br>Exposed Pads on Overall R <sub>th</sub> 111                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| Figure 8-2<br>Figure 8-3<br>Figure 8-4<br>Figure 8-5<br>Figure 8-6<br>Figure 8-7<br>Figure 8-8                                                                                     | Due to Coefficient of Thermal Expansion         (CTE) Mismatch After 1,000 Cycles         Weibull Plot Demonstrating Thinner Printed         Boards Result in Higher Fatigue Life         Hand Size Effect on Fatigue Life of 7-mm         BTC Packages         109         Plate-Up Bumped Option         109         Stand-Off Effect on Fatigue Life of a BTC         Joints         110         Calculation of Void Influence Within         Exposed Pads on Overall R <sub>th</sub> Acceptable Thermal Pad Voiding Levels         (< 30 % by Cross-Sectional Area)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| Figure 8-2<br>Figure 8-3<br>Figure 8-4<br>Figure 8-5<br>Figure 8-6<br>Figure 8-7<br>Figure 8-8<br>Figure 9-1                                                                       | Due to Coefficient of Thermal Expansion<br>(CTE) Mismatch After 1,000 Cycles       107         Weibull Plot Demonstrating Thinner Printed<br>Boards Result in Higher Fatigue Life       108         Land Size Effect on Fatigue Life of 7-mm<br>BTC Packages       109         Plate-Up Bumped Option       109         Stand-Off Effect on Fatigue Life of a BTC       109         Heat Transfer with Exposed Pad Solder<br>Joints       110         Calculation of Void Influence Within<br>Exposed Pads on Overall R <sub>th</sub> 111         Acceptable Thermal Pad Voiding Levels<br>(< 30 % by Cross-Sectional Area)                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
| Figure 8-2<br>Figure 8-3<br>Figure 8-4<br>Figure 8-5<br>Figure 8-6<br>Figure 8-7<br>Figure 8-8<br>Figure 9-1<br>Figure 9-2                                                         | Due to Coefficient of Thermal Expansion         (CTE) Mismatch After 1,000 Cycles         Weibull Plot Demonstrating Thinner Printed         Boards Result in Higher Fatigue Life         Intervention         Boards Result in Higher Fatigue Life of 7-mm         BTC Packages         Intervention         BTC Packages         Intervention         Btrace         Intervention         Intervention         Btrace         Intervention         Stand-Off Effect on Fatigue Life of a BTC         Intervention         Intervention |
| Figure 8-2<br>Figure 8-3<br>Figure 8-4<br>Figure 8-5<br>Figure 8-6<br>Figure 8-7<br>Figure 8-8<br>Figure 9-1<br>Figure 9-2<br>Figure 9-3                                           | Due to Coefficient of Thermal Expansion         (CTE) Mismatch After 1,000 Cycles       107         Weibull Plot Demonstrating Thinner Printed         Boards Result in Higher Fatigue Life       108         Land Size Effect on Fatigue Life of 7-mm         BTC Packages       109         Plate-Up Bumped Option       109         Stand-Off Effect on Fatigue Life of a BTC       109         Heat Transfer with Exposed Pad Solder       110         Joints       110         Calculation of Void Influence Within       111         Acceptable Thermal Pad Voiding Levels       111         (< 30 % by Cross-Sectional Area)                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| Figure 8-2<br>Figure 8-3<br>Figure 8-4<br>Figure 8-5<br>Figure 8-6<br>Figure 8-7<br>Figure 8-8<br>Figure 9-1<br>Figure 9-2<br>Figure 9-3<br>Figure 9-4                             | Due to Coefficient of Thermal Expansion         (CTE) Mismatch After 1,000 Cycles       107         Weibull Plot Demonstrating Thinner Printed       108         Land Size Effect on Fatigue Life of 7-mm       109         Plate-Up Bumped Option       109         Stand-Off Effect on Fatigue Life of a BTC       109         Heat Transfer with Exposed Pad Solder       110         Calculation of Void Influence Within       111         Acceptable Thermal Pad Voiding Levels       111         (< 30 % by Cross-Sectional Area)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| Figure 8-2<br>Figure 8-3<br>Figure 8-4<br>Figure 8-5<br>Figure 8-6<br>Figure 8-7<br>Figure 8-7<br>Figure 9-1<br>Figure 9-1<br>Figure 9-2<br>Figure 9-3<br>Figure 9-4<br>Figure 9-5 | Due to Coefficient of Thermal Expansion         (CTE) Mismatch After 1,000 Cycles       107         Weibull Plot Demonstrating Thinner Printed       108         Land Size Effect on Fatigue Life of 7-mm       109         Plate-Up Bumped Option       109         Stand-Off Effect on Fatigue Life of a BTC       109         Heat Transfer with Exposed Pad Solder       110         Calculation of Void Influence Within       111         Acceptable Thermal Pad Voiding Levels       113         (< 30 % by Cross-Sectional Area)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |
| Figure 8-2<br>Figure 8-3<br>Figure 8-4<br>Figure 8-5<br>Figure 8-6<br>Figure 8-7<br>Figure 8-7<br>Figure 8-8<br>Figure 9-1<br>Figure 9-2<br>Figure 9-3<br>Figure 9-4<br>Figure 9-5 | Due to Coefficient of Thermal Expansion         (CTE) Mismatch After 1,000 Cycles                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            |

| Insufficient Solder in Joint in QFN  | 116                                                                                                                     |
|--------------------------------------|-------------------------------------------------------------------------------------------------------------------------|
| Open Due to Tilted BTC               | 117                                                                                                                     |
| No Side Fillet                       | 117                                                                                                                     |
| Microvoids in a BTC I/O Solder Joint | 117                                                                                                                     |
|                                      | Insufficient Solder in Joint in QFN<br>Open Due to Tilted BTC<br>No Side Fillet<br>Microvoids in a BTC I/O Solder Joint |

#### Tables

| Table 3-1  | Sample Listing of Some Common BTC Designations                                                                            | 5    |
|------------|---------------------------------------------------------------------------------------------------------------------------|------|
| Table 3-2  | Total Cost of Ownership of BTCs                                                                                           | 9    |
| Table 4-1  | Commercially Available Quad-Flat No-<br>Lead (QFN) and Dual-Flat No-Lead (DFN)<br>Package Configurations                  | . 15 |
| Table 4-2  | Terminal Width Variations for Small Outline<br>No-Lead (SON) and Quad Flat No-Lead<br>(QFN) Packages                      | . 20 |
| Table 4-3  | Body Outline and Maximum Terminal Count                                                                                   | . 21 |
| Table 4-4  | Contact Geometries                                                                                                        | . 25 |
| Table 4-5  | Lead Frame Package Defects and Failure Modes                                                                              | . 31 |
| Table 4-6  | Plating Systems Used on Metal Lead<br>Frames                                                                              | . 32 |
| Table 4-7  | Typical Package Outline and I/O for QFN                                                                                   | . 35 |
| Table 4-8  | Leadless Lead Frame Package (LLP)<br>and Lead Frame Chip Scale Package<br>(LFCSP) Contact Pitch and Width Variations      | . 37 |
| Table 4-9  | Basic Material Elements for the Leadless Lead<br>Frame Package (LLP) and Lead Frame Chip<br>Scale Package (LFCSP) Devices | . 37 |
| Table 4-10 | Substrate-Based Land Grid Array (LGA)<br>Defects and Failure Modes                                                        | . 43 |
| Table 5-1  | Key Attributed of Various Printed Board<br>Surface Finishes                                                               | . 45 |
| Table 6-1  | Common BTC Technology Ranges                                                                                              | . 51 |
| Table 6-2  | BTC Thermal Pad Design Option Pros<br>and Cons                                                                            | . 66 |
| Table 6-3  | Solder Mask Defined (SMD) Design Point Parameters                                                                         | . 69 |
| Table 6-4  | Open-Cu Thermal Pad Design Parameters                                                                                     | . 73 |
| Table 6-5  | Via Tenting Thermal Pad Design<br>Parameters                                                                              | . 75 |
| Table 6-6  | Via-in-Pad Plated-Over (VIPPO) Design<br>Parameters                                                                       | . 77 |
| Table 6-7  | Floating Solder Mask Design Parameters                                                                                    | . 79 |
| Table 7-1  | Particle Size Comparisons                                                                                                 | . 92 |
| Table 7-2  | Pros and Cons of Common Stencil<br>Technologies and Options                                                               | . 94 |
| Table 7-3  | Design and Process Control Points for BTCs                                                                                | . 99 |
| Table 7-4  | Production-Level Inspection Stages Ensuring<br>High-Quality BTC Manufacturing                                             | . 99 |

## Design and Assembly Process Implementation for Bottom Termination Components (BTCs)

### 1 SCOPE

This standard describes design and assembly guidance for implementing bottom termination components (BTCs). The focus of the information contained herein is on critical design, materials, assembly, inspection, repair, quality and reliability issues associated with BTCs.

This standard applies only to BTCs, which are components with planar terminations under the body with or without wettable side terminations or flanks. Examples of BTCs include small-outline no-lead (SON), dual-flat no-lead (DFN), quad-flat no-lead (QFN), land grid array (LGA), etc. (see Section 4).

1.1 Purpose The purpose of this standard is to provide useful and practical information to those who use or are consid-

ering using BTCs. The target audiences for this document are physical designers, process engineers, reliability engineers and managers who are responsible for design, assembly, inspection and repair processes of printed boards and printed board assemblies. Information described in this standard enables high-quality and highly reliable BTC assembled devices operating within an electronic system.

This document also describes how to successfully implement robust design and assembly processes for printed board assemblies using BTCs as well as ways to troubleshoot some common anomalies which can occur during BTC assembly. For accept/reject criteria and requirements for BTC assemblies, see J-STD-001 and IPC-A-610.



Figure 1-1 Various Forms of BTCs

Figure 1-1 provides an example of various forms of BTCs.

**1.2 Classification** IPC standards recognize that electrical and electronic assemblies are subject to classifications by intended end-item use. Three general end-product classes have been established to reflect differences in manufacturability, complexity, functional performance requirements and verification (inspection/test) frequency. It should be recognized that there may be overlaps of equipment between classes.

### **CLASS 1** General Electronic Products

Includes products suitable for applications where the major requirement is function of the completed assembly.

### **CLASS 2 Dedicated Service Electronic Products**

Includes products where continued performance and extended life is required, and for which uninterrupted service is desired but not critical. Typically, the end-use environment would not cause failures.

### CLASS 3 High Performance/Harsh Environment Electronic Products

Includes products where continued high performance or performance-on-demand is critical, equipment downtime cannot be tolerated, end-use environment may be uncommonly harsh, and the equipment must function when required, such as life support or other critical systems.

**1.3 Measurement Units** All dimensions and tolerances in this specification are expressed in hard SI (metric) units. Users of this specification are expected to use metric dimensions. All dimensions  $\geq 1.0$  mm will be expressed in mm. All dimensions < 1.0 mm will be expressed in  $\mu$ m.

**1.4 Definition of Requirements** The words **shall** or **shall not** are used in the text of this document wherever there is a requirement for materials, preparation, process control or acceptance. The word "should" reflects recommendations and is used to reflect general industry practices and procedures for guidance only.