



IPC-6012C-2010

Qualification and Performance Specification for Rigid Printed Boards

Developed by the Rigid Printed Board Performance Specifications Task Group (D-33a) of the Rigid Printed Board Committee (D-30) of IPC

Supersedes:

IPC-6012B with
Amendment 1 - July 2007
IPC-6012B - August 2004
IPC-6012A with
Amendment 1 - July 2000
IPC-6012A - October 1999
IPC-6012 - July 1996
IPC-RB-276 - March 1992

Users of this publication are encouraged to participate in the development of future revisions.

Contact:

IPC
3000 Lakeside Drive, Suite 309S
Bannockburn, Illinois
60015-1249
Tel 847 615.7100
Fax 847 615.7105

Table of Contents

1	SCOPE	1	3.2.12	Heatsink Planes, External	11
1.1	Statement of Scope	1	3.2.13	Via Protection	11
1.2	Purpose	1	3.2.14	Embedded Passive Materials	12
1.2.1	Supporting Documentation	1	3.3	Visual Examination	12
1.3	Performance Classification and Type	1	3.3.1	Edges	12
1.3.1	Classification	1	3.3.2	Laminate Imperfections	12
1.3.2	Printed Board Type	1	3.3.3	Plating and Coating Voids in the Hole	13
1.3.3	Selection for Procurement	1	3.3.4	Lifted Lands	13
1.3.4	Material, Plating Process and Final Finish	3	3.3.5	Marking	13
1.4	Terms and Definitions	4	3.3.6	Solderability	13
1.4.1	As Agreed Between User and Supplier (AABUS)	4	3.3.7	Plating Adhesion	14
1.5	Interpretation	4	3.3.8	Edge Printed Board Contact, Junction of Gold Plate to Solder Finish	14
1.6	Presentation	4	3.3.9	Workmanship	14
1.7	Revision Level Changes	4	3.4	Printed Board Dimensional Requirements	14
2	APPLICABLE DOCUMENTS	4	3.4.1	Hole Size, Hole Pattern Accuracy and Pattern Feature Accuracy	14
2.1	IPC	4	3.4.2	Annular Ring and Breakout (External)	15
2.2	Joint Industry Standards	6	3.4.3	Bow and Twist	16
2.3	Federal	6	3.5	Conductor Definition	16
2.4	Other Publications	6	3.5.1	Conductor Width and Thickness	16
2.4.1	American Society for Testing and Materials	6	3.5.2	Conductor Spacing	16
2.4.2	Underwriters Lab	7	3.5.3	Conductor Imperfections	16
2.4.3	National Electrical Manufacturers Association	7	3.5.4	Conductive Surfaces	16
2.4.4	American Society for Quality	7	3.6	Structural Integrity	18
2.4.5	AMS	7	3.6.1	Thermal Stress Testing	18
2.4.6	American Society of Mechanical Engineers	7	3.6.2	Requirements for Microsectioned Coupons or Printed Boards	19
3	REQUIREMENTS	7	3.7	Solder Mask Requirements	27
3.1	General	7	3.7.1	Solder Mask Coverage	27
3.2	Materials	7	3.7.2	Solder Mask Cure and Adhesion	28
3.2.1	Laminates and Bonding Material	7	3.7.3	Solder Mask Thickness	28
3.2.2	External Bonding Materials	7	3.8	Electrical Requirements	28
3.2.3	Other Dielectric Materials	7	3.8.1	Dielectric Withstanding Voltage	28
3.2.4	Metal Foils	7	3.8.2	Electrical Continuity and Isolation Resistance	29
3.2.5	Metal Planes/Cores	8	3.8.3	Circuit/PTH Shorts to Metal Substrate	29
3.2.6	Base Metallic Plating Depositions and Conductive Coatings	8	3.8.4	Moisture and Insulation Resistance (MIR)	29
3.2.7	Final Finish Depositions and Coatings - Metallic and Nonmetallic	8	3.9	Cleanliness	29
3.2.8	Polymer Coating (Solder Mask)	11	3.9.1	Cleanliness Prior to Solder Mask Application	29
3.2.9	Fusing Fluids and Fluxes	11	3.9.2	Cleanliness After Solder Mask, Solder, or Alternative Surface Coating Application	29
3.2.10	Marking Inks	11	3.9.3	Cleanliness of Inner Layers After Oxide Treatment Prior to Lamination	29
3.2.11	Hole Fill Insulation Material	11			

3.10 Special Requirements 29

3.10.1 Outgassing 29

3.10.2 Organic Contamination 30

3.10.3 Fungus Resistance 30

3.10.4 Vibration 30

3.10.5 Mechanical Shock 30

3.10.6 Impedance Testing 30

3.10.7 Coefficient of Thermal Expansion (CTE) 30

3.10.8 Thermal Shock 30

3.10.9 Surface Insulation Resistance (As Received) .. 30

3.10.10 Metal Core (Horizontal Microsection) 30

3.10.11 Rework Simulation 31

3.10.12 Bond Strength, Unsupported Component Hole Land 31

3.10.13 Destructive Physical Analysis 31

3.11 Repair 31

3.11.1 Circuit Repairs 31

3.12 Rework 31

4 QUALITY ASSURANCE PROVISIONS 31

4.1 General 31

4.1.1 Qualification 31

4.1.2 Sample Test Coupons 31

4.2 Acceptance Tests 32

4.2.1 C=0 Zero Acceptance Number Sampling Plan 32

4.2.2 Referee Tests 32

4.3 Quality Conformance Testing 32

4.3.1 Coupon Selection 32

5 NOTES 37

5.1 Ordering Data 37

5.2 Superseded Specifications 37

APPENDIX A 38

APPENDIX B 41

Figures

Figure 3-1 Annular Ring Measurement (External) 15

Figure 3-2 Breakout of 90° and 180° 15

Figure 3-3 Conductor Width Reduction 15

Figure 3-4 Rectangular Surface Mount Lands 17

Figure 3-5 Round Surface Mount Lands 17

Figure 3-6 Crack Definition 20

Figure 3-7 Separations at External Foil 20

Figure 3-8 Plating Folds/Inclusions - Minimum Measurement Points 21

Figure 3-9 Typical Microsection Evaluation Specimen 21

Figure 3-10 Measurement for Etchback 22

Figure 3-11 Maximum Dielectric Removal Resulting From Etchback 22

Figure 3-12 Negative Etchback 23

Figure 3-13 Annular Ring Measurement (Internal) 23

Figure 3-14 Microsection Rotations for Breakout Detection 23

Figure 3-15 Comparison of Microsection Rotations 23

Figure 3-16 Surface Copper Wrap Measurement (Applicable to all filled PTHs) 24

Figure 3-17 Wrap Copper in Type 4 Printed Board (Acceptable) 25

Figure 3-18 Wrap Copper Removed by Excessive Sanding/Planarization (Not Acceptable) 25

Figure 3-19 Copper Cap Thickness 25

Figure 3-20 Copper Cap Filled Via Height (Bump) 25

Figure 3-21 Copper Cap Depression (Dimple) 25

Figure 3-22 Copper Cap Plating Voids 25

Figure 3-23 Metal Core to Plated-Through Hole Spacing 27

Figure 3-24 Measurement of Minimum Dielectric Spacing 27

Tables

Table 1-1 Technology Adders 2

Table 1-2 Default Requirements 3

Table 3-1 Metal Planes/Cores 8

Table 3-2 Final Finish and Coating Requirements 10

Table 3-3 Surface and Hole Copper Plating Minimum Requirements for Buried Vias ≥ 2 Layers, Through-Holes and Blind Vias 11

Table 3-4 Surface and Hole Copper Plating Minimum Requirements for Microvias (Blind and Buried) 11

Table 3-5 Surface and Hole Copper Plating Minimum Requirements for Buried Via Cores (2 Layers) 11

Table 3-6 Plating and Coating Voids in the Hole 13

Table 3-7 Edge Printed Board Contact Gap 14

Table 3-8 Minimum Annular Ring 15

Table 3-9 Plated Hole Integrity After Stress 19

Table 3-10 Cap Plating Requirements 25

Table 3-11 Internal Layer Foil Thickness after Processing 26

Table 3-12 External Conductor Thickness after Plating 26

Table 3-13 Solder Resist Adhesion 28

Table 3-14 Dielectric Withstanding Voltages 29

Table 3-15 Insulation Resistance 29

Table 4-1 Qualification Test Coupons 32

Table 4-2 C=0 Sampling Plan (Sample Size for Specific Index Value) 33

Table 4-3 Acceptance Testing and Frequency 33

Table 4-4 Quality Conformance Testing 37

Table A.1 Class 3/A Supplemental Requirements 38

Qualification and Performance Specification for Rigid Printed Boards

1 SCOPE

1.1 Statement of Scope This specification establishes and defines the qualification and performance requirements for the fabrication of rigid printed boards.

1.2 Purpose The purpose of this specification is to provide requirements for qualification and performance of rigid printed boards based on the following constructions and/or technologies:

- Single-sided, double-sided printed boards with or without plated-through holes (PTHs).
- Multilayer printed boards with PTHs with or without buried/blind vias.
- Multilayer printed boards containing build up High Density Interconnect (HDI) layers conforming to IPC-6016.
- Active embedded passive circuitry printed boards with distributive capacitive planes and/or capacitive or resistive components.
- Metal core printed boards with or without an external metal heat frame, which may be active or nonactive.

1.2.1 Supporting Documentation IPC-A-600, which contains figures, illustrations and photographs that can aid in the visualization of externally and internally observable acceptable/nonconforming conditions, may be used in conjunction with this specification for a more complete understanding of the recommendations and requirements.

1.3 Performance Classification and Type

1.3.1 Classification This specification establishes acceptance criteria for the performance classification of rigid printed boards based on customer and/or end-use requirements. Printed boards are classified by one of three general Performance Classes as defined in IPC-6011.

1.3.1.1 Requirement Deviations Requirements deviating from these heritage classifications **shall** be as agreed between user and supplier (AABUS).

1.3.1.2 Space and Military Avionics Deviations Space and Military Avionics performance classification deviations are defined and listed in Appendix A of this specification. These are commonly referred to as Class 3/A.

1.3.2 Printed Board Type Printed boards without PTHs (Type 1) and with PTHs (Types 2-6) are classified as follows:

Type 1—Single-Sided Printed Board

Type 2—Double-Sided Printed Board

Type 3—Multilayer Printed Board without blind or buried vias

Type 4—Multilayer Printed Board with blind and/or buried vias

Type 5—Multilayer metal core Printed Board without blind or buried vias

Type 6—Multilayer metal core Printed Board with blind and/or buried vias

1.3.3 Selection for Procurement Performance class **shall** be specified in the procurement documentation.

The procurement documentation **shall** provide sufficient information to fabricate the printed board and ensure that the user receives the desired product. Information that should be included in the procurement documentation is to be in accordance with IPC-D-325.