

IPC-7095B

# Design and Assembly Process Implementation for BGAs

Developed by the Device Manufacturers Interface Committee of IPC



Supersedes:

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# **Table of Contents**

1 SC	COPE
1.1	Purpose 1
1.2	Intent 1
2 AI	PPLICABLE DOCUMENTS 1
2.1	IPC
2.2	JEDEC 1
3 SE BO	ELECTION CRITERIA AND MANAGING GA IMPLEMENTATION
3.1	Description of Infrastructure 3
3.1.1	Land Patterns and Circuit Board Considerations
3.1.2	Technology Comparison 5
3.1.3	Assembly Equipment Impact 7
3.1.4	Stencil Requirements 7
3.1.5	Inspection Requirements
3.1.6	Test
3.2	Time-to-Market Readiness 8
3.3	Methodology9
3.4	Process Step Analysis
3.5	BGA Limitations and Issues
3.5.1	Visual Inspection
3.5.2	Moisture Sensitivity
3.5.3	Thermally Unbalanced BGA Design 10
3.5.4	Rework 10
3.5.5	Cost 11
3.5.6	Availability 12
3.5.7	Voids in BGA 12
3.5.8	Standardization Issues 12
3.5.9	Reliability Concerns 12
4 C0	DMPONENT CONSIDERATIONS 12
4.1	Component Packaging Comparisons and Drivers 12
4.1.1	Package Feature Comparisons 12
4.1.2	BGA Package Drivers 13
4.1.3	Cost Issues 13
4.1.4	Component Handling 13
4.1.5	Thermal Performance 13
4.1.6	Real Estate 13
4.1.7	Electrical Performance 14
4.2	Die Mounting in the BGA Package 14
4.2.1	Wire Bond 14
4.2.2	Flip Chip 15
4.3	Standardization 16

4.3.1	Industry Standards for BGA	16
4.3.2	Ball Pitch	17
4.3.3	BGA Package Outline	18
4.3.4	Ball Size Relationships	19
4.3.5	Coplanarity	19
4.4	Component Packaging Style Considerations	19
4.4.1	Solder Ball Alloy	20
4.4.2	Ball Attach Process	20
4.4.3	Ceramic Ball Grid Array	21
4.4.4	Ceramic Column Grid Arrays	21
4.4.5	Tape Ball Grid Arrays	22
4.4.6	Multiple Die Packaging	22
4.4.7	System-in-Package (SiP)	23
4.4.8	3D Folded Package Technology	23
4.4.9	Ball Stack, Package-on-Package	23
4.4.10	Folded and Stacked Packaging Combination	24
4.4.11	Benefits of Multiple Die Packaging	24
4.5	BGA Connectors	24
4.5.1	Material Considerations for BGA Connectors	24
4.5.2	Attachment Considerations for BGA Connectors	25
4.6	BGA Construction Materials	25
4.6.1	Types of Substrate Materials	25
4.6.2	Properties of Substrate Materials	26
4.7	BGA Package Design Considerations	27
4.7.1	Power and Ground Planes	27
4.7.2	Signal Integrity	28
4.7.3	Heat Spreader Incorporation Inside the Package	28
4.8	BGA Package Acceptance Criteria and	20
401	Simpping Format	20
4.8.1		28
4.8.2	Voids in Solder Balls	28
4.8.3	Solder Ball Attach Integrity	29
4.8.4	Package Coplanarity	29
4.8.5	Moisture Sensitivity (Baking, Storage, Handling, Rebaking)	30
4.8.6	Shipping Medium (Tape and Reel, Trays, Tubes)	30
4.8.7	Solder Ball Alloy	31
5 PC	BS AND OTHER MOUNTING STRUCTURES	31
5.1	Types of Mounting Structures	31
5.1.1	Organic Resin Systems	31
5.1.2	Inorganic Structures	31

5.1.3	Layering (Multilayer, Sequential or Build-Up)	31
5.2	Properties of Mounting Structures	31
5.2.1	Resin Systems	31
5.2.2	Reinforcements	33
5.2.3	Laminate Material Properties	33
5.2.4	Reliability Concerns with High Lead-Free Soldering Temperatures	33
5.2.5	Thermal Expansion	33
5.2.6	Glass Transition Temperature	33
5.2.7	Moisture Absorption	34
5.3	Surface Finishes	34
5.3.1	Hot Air Solder Leveling (HASL)	34
5.3.2	Organic Surface Protection (Organic Solderability Preservative) OSP Coatings	37
5.3.3	Noble Platings/Coatings	37
5.4	Solder Mask	40
5.4.1	Wet and Dry Film Solder Masks	41
5.4.2	Photoimageable Solder Masks	41
5.4.3	Registration	42
5.4.4	Via Protection	42
5.5	Thermal Spreader Structure Incorporation (e.g., Metal Core Boards)	44
5.5.1	Lamination Sequences	44
5.5.2	Heat Transfer Pathway	44
6 Pi Di	RINTED CIRCUIT ASSEMBLY ESIGN CONSIDERATION	46
6.1	Component Placement and Clearances	46
6.1.1	Pick and Place Requirements	46
6.1.2	Repair/Rework Requirements	46
6.1.3	Global Placement	47
6.1.4	Alignment Legends (Silkscreen, Copper Features, Pin 1 Identifier)	47
6.2	Attachment Sites (Land Patterns and Vias)	48
6.2.1	Big vs. Small Land and Impact on Routing	48
6.2.2	Solder Mask vs. Metal Defined Land Design	48
6.2.3	Conductor Width	50
6.2.4	Via Size and Location	50
6.3	Escape and Conductor Routing Strategies	51
6.3.1	Escape Strategies	53
6.3.2	Surface Conductor Details	54
6.3.3	Dog Bone Through Via Details	54
6.3.4	Design for Mechanical Strain	54
6.3.5	Uncapped Via-in-Pad and Impact on	
0.0.0	Reliability Issues	55
6.3.6		56
	Fine Pitch BGA Microvia in Pad Strategies	30
6.3.7	Power and Ground Connectivity	50 57

## March 2008

	Impact of Wave Solder on Top Side BGAs	57
6.4.1	Top Side Reflow	57
6.4.2	Impact of Top Side Reflow	57
6.4.3	Methods of Avoiding Top Side Reflow	58
6.4.4	Top Side Reflow for Lead-Free Boards	59
6.5	Testability and Test Point Access	59
6.5.1	Component Testing	59
6.5.2	Damage to the Solder Balls During Test and Burn-In	60
6.5.3	Bare Board Testing	61
6.5.4	Assembly Testing	61
6.6	Other Design for Manufacturability Issues	64
6.6.1	Panel/Subpanel Design	64
6.6.2	In-Process/End Product Test Coupons	64
6.7	Thermal Management	65
6.7.1	Conduction	65
6.7.2	Radiation	66
6.7.3	Convection	67
6.7.4	Thermal Interface Materials	67
6.7.5	Heat Sink Attachment Methods for BGAs	67
6.8	Documentation and Electronic Data Transfer	69
6.8.1	Drawing Requirements	69
6.8.2	Equipment Messaging Protocols	70
6.8.3	Specifications	71
7 AS	SEMBLY OF BGAS ON PRINTED	71
		/ 1
71	SMT Assembly Processes	71
7.1 7.1.1	Solder Paste and Its Application	71 71
7.1 7.1.1 7.1.2	Solder Paste and Its Application	71 71 73
7.1 7.1.1 7.1.2 7.1.3	Solder Paste and Its Application Component Placement Impact	71 71 73 72
7.1 7.1.1 7.1.2 7.1.3	Solder Paste and Its Application Component Placement Impact Vision Systems for Placement	<ul> <li>71</li> <li>71</li> <li>73</li> <li>73</li> <li>74</li> </ul>
7.1 7.1.1 7.1.2 7.1.3 7.1.4 7.1.5	SMT Assembly Processes Solder Paste and Its Application Component Placement Impact Vision Systems for Placement Reflow Soldering and Profiling	<ul> <li>71</li> <li>71</li> <li>73</li> <li>73</li> <li>74</li> <li>78</li> </ul>
7.1 7.1.1 7.1.2 7.1.3 7.1.4 7.1.5 7.1.6	SMT Assembly Processes Solder Paste and Its Application Component Placement Impact Vision Systems for Placement Reflow Soldering and Profiling Material Issues	<ul> <li>71</li> <li>71</li> <li>73</li> <li>73</li> <li>74</li> <li>78</li> <li>70</li> </ul>
7.1 7.1.1 7.1.2 7.1.3 7.1.4 7.1.5 7.1.6	SMT Assembly Processes Solder Paste and Its Application Component Placement Impact Vision Systems for Placement Reflow Soldering and Profiling Material Issues Vapor Phase	<ul> <li>71</li> <li>73</li> <li>73</li> <li>74</li> <li>78</li> <li>78</li> <li>78</li> <li>78</li> </ul>
7.1 7.1.1 7.1.2 7.1.3 7.1.4 7.1.5 7.1.6 7.1.7	SMT Assembly Processes Solder Paste and Its Application Component Placement Impact Vision Systems for Placement Reflow Soldering and Profiling Material Issues Vapor Phase Cleaning vs. No-Clean	<ul> <li>71</li> <li>71</li> <li>73</li> <li>73</li> <li>74</li> <li>78</li> <li>78</li> <li>79</li> <li>70</li> </ul>
7.1 7.1.1 7.1.2 7.1.3 7.1.4 7.1.5 7.1.6 7.1.7 7.1.8	SMT Assembly Processes Solder Paste and Its Application Component Placement Impact Vision Systems for Placement Reflow Soldering and Profiling Material Issues Vapor Phase Cleaning vs. No-Clean Package Standoff	<ol> <li>71</li> <li>71</li> <li>73</li> <li>73</li> <li>74</li> <li>78</li> <li>78</li> <li>79</li> <li>79</li> </ol>
7.1 7.1.1 7.1.2 7.1.3 7.1.4 7.1.5 7.1.6 7.1.7 7.1.8 7.2	SMT Assembly Processes Solder Paste and Its Application Component Placement Impact Vision Systems for Placement Reflow Soldering and Profiling Material Issues Vapor Phase Cleaning vs. No-Clean Package Standoff Post-SMT Processes	<ol> <li>71</li> <li>71</li> <li>73</li> <li>73</li> <li>74</li> <li>78</li> <li>78</li> <li>79</li> <li>79</li> <li>80</li> </ol>
7.1 7.1.1 7.1.2 7.1.3 7.1.4 7.1.5 7.1.6 7.1.7 7.1.8 7.2 7.2.1	SMT Assembly Processes Solder Paste and Its Application Component Placement Impact Vision Systems for Placement Reflow Soldering and Profiling Material Issues Vapor Phase Cleaning vs. No-Clean Package Standoff Post-SMT Processes Conformal Coatings	<ol> <li>71</li> <li>71</li> <li>73</li> <li>73</li> <li>74</li> <li>78</li> <li>78</li> <li>79</li> <li>79</li> <li>80</li> <li>80</li> </ol>
7.1 7.1.1 7.1.2 7.1.3 7.1.4 7.1.5 7.1.6 7.1.7 7.1.8 7.2 7.2.1 7.2.2	SMT Assembly Processes Solder Paste and Its Application Component Placement Impact Vision Systems for Placement Reflow Soldering and Profiling Material Issues Vapor Phase Cleaning vs. No-Clean Package Standoff Post-SMT Processes Conformal Coatings Use of Underfills and Adhesives	<ol> <li>71</li> <li>71</li> <li>73</li> <li>73</li> <li>74</li> <li>78</li> <li>78</li> <li>79</li> <li>79</li> <li>80</li> <li>80</li> <li>81</li> </ol>
7.1 7.1.1 7.1.2 7.1.3 7.1.4 7.1.5 7.1.6 7.1.7 7.1.8 7.2 7.2.1 7.2.2 7.2.3	SMIT Assembly Processes Solder Paste and Its Application Component Placement Impact Vision Systems for Placement Reflow Soldering and Profiling Material Issues Vapor Phase Cleaning vs. No-Clean Package Standoff Post-SMT Processes Conformal Coatings Use of Underfills and Adhesives Depaneling of Boards and Modules	<ul> <li>71</li> <li>71</li> <li>73</li> <li>74</li> <li>78</li> <li>79</li> <li>79</li> <li>80</li> <li>80</li> <li>81</li> <li>84</li> </ul>
7.1 7.1.1 7.1.2 7.1.3 7.1.4 7.1.5 7.1.6 7.1.7 7.1.8 7.2 7.2.1 7.2.2 7.2.3 7.3	SMT Assembly Processes Solder Paste and Its Application Component Placement Impact Vision Systems for Placement Reflow Soldering and Profiling Material Issues Vapor Phase Cleaning vs. No-Clean Package Standoff Post-SMT Processes Conformal Coatings Use of Underfills and Adhesives Depaneling of Boards and Modules Inspection Techniques	<ul> <li>71</li> <li>71</li> <li>73</li> <li>73</li> <li>74</li> <li>78</li> <li>78</li> <li>79</li> <li>79</li> <li>80</li> <li>80</li> <li>81</li> <li>84</li> <li>84</li> </ul>
7.1 7.1.1 7.1.2 7.1.3 7.1.4 7.1.5 7.1.6 7.1.7 7.1.8 7.2 7.2.1 7.2.2 7.2.3 7.3 7.3.1	SMIT Assembly Processes Solder Paste and Its Application Component Placement Impact Vision Systems for Placement Reflow Soldering and Profiling Material Issues Vapor Phase Cleaning vs. No-Clean Package Standoff Post-SMT Processes Conformal Coatings Use of Underfills and Adhesives Depaneling of Boards and Modules Inspection Techniques X-Ray Usage	<ul> <li>71</li> <li>71</li> <li>73</li> <li>74</li> <li>78</li> <li>78</li> <li>79</li> <li>79</li> <li>80</li> <li>81</li> <li>84</li> <li>84</li> <li>84</li> </ul>
7.1 7.1.1 7.1.2 7.1.3 7.1.4 7.1.5 7.1.6 7.1.7 7.1.8 7.2 7.2.1 7.2.2 7.2.3 7.3 7.3.1 7.3.2	SMT Assembly Processes Solder Paste and Its Application Component Placement Impact Vision Systems for Placement Reflow Soldering and Profiling Material Issues Vapor Phase Cleaning vs. No-Clean Package Standoff Post-SMT Processes Conformal Coatings Use of Underfills and Adhesives Depaneling of Boards and Modules Inspection Techniques X-Ray Usage X-Ray Image Acquisition	<ul> <li>71</li> <li>71</li> <li>73</li> <li>73</li> <li>74</li> <li>78</li> <li>78</li> <li>79</li> <li>79</li> <li>80</li> <li>80</li> <li>81</li> <li>84</li> <li>84</li> <li>84</li> <li>85</li> </ul>
7.1 7.1.1 7.1.2 7.1.3 7.1.4 7.1.5 7.1.6 7.1.7 7.1.8 7.2 7.2.1 7.2.2 7.2.3 7.3 7.3.1 7.3.2 7.3.3	SMIT Assembly Processes Solder Paste and Its Application Component Placement Impact Vision Systems for Placement Reflow Soldering and Profiling Material Issues Vapor Phase Cleaning vs. No-Clean Package Standoff Post-SMT Processes Conformal Coatings Use of Underfills and Adhesives Depaneling of Boards and Modules Inspection Techniques X-Ray Usage X-Ray Image Acquisition Definition and Discussion of X-Ray System Terminology	<ul> <li>71</li> <li>71</li> <li>73</li> <li>73</li> <li>74</li> <li>78</li> <li>78</li> <li>79</li> <li>80</li> <li>80</li> <li>81</li> <li>84</li> <li>84</li> <li>84</li> <li>85</li> <li>86</li> </ul>
7.1 7.1.1 7.1.2 7.1.3 7.1.4 7.1.5 7.1.6 7.1.7 7.1.8 7.2 7.2.1 7.2.2 7.2.3 7.3 7.3.1 7.3.2 7.3.3 7.3.4	SMT Assembly Processes Solder Paste and Its Application Component Placement Impact Vision Systems for Placement Reflow Soldering and Profiling Material Issues Vapor Phase Cleaning vs. No-Clean Package Standoff Package Standoff Post-SMT Processes Conformal Coatings Use of Underfills and Adhesives Depaneling of Boards and Modules Inspection Techniques X-Ray Usage X-Ray Image Acquisition Definition and Discussion of X-Ray System Terminology Analysis of the X-Ray Image	<ul> <li>71</li> <li>71</li> <li>73</li> <li>74</li> <li>78</li> <li>78</li> <li>79</li> <li>79</li> <li>80</li> <li>80</li> <li>81</li> <li>84</li> <li>84</li> <li>84</li> <li>85</li> <li>86</li> <li>88</li> </ul>

7.3.6	BGA Standoff Measurement 90
7.3.7	Optical Inspection 90
7.3.8	Destructive Analysis Methods
7.4	Testing and Product Verification
7.4.1	Electrical Testing
7.4.2	Test Coverage
7.4.3	Burn-In Testing
7.4.4	Product Screening Tests
7.5	Assembly Process Control Criteria for Plastic BGAs
7.5.1	Voids
7.5.2	Solder Bridging 106
7.5.3	Opens 106
7.5.4	Cold Solder 106
7.5.5	Defect Correlation/Process Improvement 106
7.5.6	Insufficient/Uneven Heating 107
7.5.7	Component Defects 107
7.6	Repair Processes 108
7.6.1	Rework/Repair Philosophy 108
7.6.2	Removal of BGA 108
7.6.3	Replacement 109
8 RI	ELIABILITY
0 1	
0.1	Accelerated Reliability Testing 111
8.1 8.2	Accelerated Reliability Testing 111 Damage Mechanisms and Failure of Solder Attachments
8.1 8.2 8.2.1	Accelerated Reliability Testing
8.1 8.2 8.2.1 8.2.2	Accelerated Reliability Testing
8.1 8.2 8.2.1 8.2.2 8.3	Accelerated Reliability Testing
8.1 8.2 8.2.1 8.2.2 8.3 8.3.1	Accelerated Reliability Testing111Damage Mechanisms and Failure of Solder Attachments112Comparison of Thermal Fatigue Crack Growth Mechanism in SAC vs. Tin/ Lead BGA Solder Joints113Mixed Alloy Soldering113Solder Joints and Attachment Types115Global Expansion Mismatch116
8.1 8.2 8.2.1 8.2.2 8.3 8.3.1 8.3.2	Accelerated Reliability Testing111Damage Mechanisms and Failure of Solder Attachments112Comparison of Thermal Fatigue Crack Growth Mechanism in SAC vs. Tin/ Lead BGA Solder Joints113Mixed Alloy Soldering113Solder Joints and Attachment Types115Global Expansion Mismatch116Local Expansion Mismatch116
8.1 8.2 8.2.1 8.2.2 8.3 8.3.1 8.3.2 8.3.3	Accelerated Reliability Testing111Damage Mechanisms and Failure of Solder Attachments112Comparison of Thermal Fatigue Crack Growth Mechanism in SAC vs. Tin/ Lead BGA Solder Joints113Mixed Alloy Soldering113Solder Joints and Attachment Types115Global Expansion Mismatch116Local Expansion Mismatch116Internal Expansion Mismatch116
8.1 8.2 8.2.1 8.2.2 8.3 8.3.1 8.3.2 8.3.3 8.4	Accelerated Reliability Testing111Damage Mechanisms and Failure of Solder Attachments112Comparison of Thermal Fatigue Crack Growth Mechanism in SAC vs. Tin/ Lead BGA Solder Joints113Mixed Alloy Soldering113Solder Joints and Attachment Types115Global Expansion Mismatch116Local Expansion Mismatch116Solder Attachment Failure116
8.1 8.2 8.2.1 8.2.2 8.3 8.3.1 8.3.2 8.3.3 8.4 8.4.1	Accelerated Reliability Testing111Damage Mechanisms and Failure of Solder Attachments112Comparison of Thermal Fatigue Crack Growth Mechanism in SAC vs. Tin/ Lead BGA Solder Joints113Mixed Alloy Soldering113Solder Joints and Attachment Types115Global Expansion Mismatch116Local Expansion Mismatch116Solder Attachment Failure116Solder Attachment Failure116
<ul> <li>8.1</li> <li>8.2</li> <li>8.2.1</li> <li>8.2.2</li> <li>8.3</li> <li>8.3.1</li> <li>8.3.2</li> <li>8.3.3</li> <li>8.4</li> <li>8.4.1</li> <li>8.4.2</li> </ul>	Accelerated Reliability Testing111Damage Mechanisms and Failure of112Solder Attachments112Comparison of Thermal Fatigue CrackGrowth Mechanism in SAC vs. Tin/Lead BGA Solder Joints113Mixed Alloy Soldering113Solder Joints and Attachment Types115Global Expansion Mismatch116Local Expansion Mismatch116Internal Expansion Mismatch116Solder Attachment Failure116Solder Attachment Failure116
8.1 8.2 8.2.1 8.2.2 8.3 8.3.1 8.3.2 8.3.3 8.4 8.4.1 8.4.2 8.4.3	Accelerated Reliability Testing111Damage Mechanisms and Failure of Solder Attachments112Comparison of Thermal Fatigue Crack Growth Mechanism in SAC vs. Tin/ Lead BGA Solder Joints113Mixed Alloy Soldering113Solder Joints and Attachment Types115Global Expansion Mismatch116Local Expansion Mismatch116Solder Attachment Failure116Solder Attachment Failure116Solder Attachment Failure117Failure Signature-1:Cold SolderLead Solder Attachment Failure117
<ul> <li>8.1</li> <li>8.2</li> <li>8.2.1</li> <li>8.2.2</li> <li>8.3</li> <li>8.3.1</li> <li>8.3.2</li> <li>8.3.3</li> <li>8.4</li> <li>8.4.1</li> <li>8.4.2</li> <li>8.4.3</li> <li>8.4.4</li> </ul>	Accelerated Reliability Testing111Damage Mechanisms and Failure of112Solder Attachments112Comparison of Thermal Fatigue CrackGrowth Mechanism in SAC vs. Tin/Lead BGA Solder Joints113Mixed Alloy Soldering113Solder Joints and Attachment Types115Global Expansion Mismatch116Local Expansion Mismatch116Solder Attachment Failure116Solder Attachment Failure116Failure Signature-1:Cold SolderCold Solder117Failure Signature-3:Ball DropMixel Signature-1:117
8.1 8.2 8.2.1 8.2.2 8.3 8.3.1 8.3.2 8.3.3 8.4 8.4.1 8.4.2 8.4.3 8.4.4 8.4.5	Accelerated Reliability Testing111Damage Mechanisms and Failure of112Solder Attachments112Comparison of Thermal Fatigue CrackGrowth Mechanism in SAC vs. Tin/Lead BGA Solder Joints113Mixed Alloy Soldering113Solder Joints and Attachment Types115Global Expansion Mismatch116Local Expansion Mismatch116Solder Attachment Failure116Solder Attachment Failure116Solder Attachment Failure117Failure Signature-1:Cold SolderCold Solder117Failure Signature-3:Ball Drop118
<ul> <li>8.1</li> <li>8.2</li> <li>8.2.1</li> <li>8.2.2</li> <li>8.3</li> <li>8.3.1</li> <li>8.3.2</li> <li>8.3.3</li> <li>8.4</li> <li>8.4.1</li> <li>8.4.2</li> <li>8.4.3</li> <li>8.4.4</li> <li>8.4.5</li> <li>8.4.6</li> </ul>	Accelerated Reliability Testing111Damage Mechanisms and Failure of112Solder Attachments112Comparison of Thermal Fatigue CrackGrowth Mechanism in SAC vs. Tin/Lead BGA Solder Joints113Mixed Alloy Soldering113Solder Joints and Attachment Types115Global Expansion Mismatch116Local Expansion Mismatch116Solder Attachment Failure116Solder Attachment Failure116Solder Attachment Failure116Failure Signature-1:Cold SolderFailure Signature-2:Land, Nonsolderable117Failure Signature-3:Ball Drop117Failure Signature-4:Missing BallFailure Signature-5:Package Warpage118Failure Signature-5:
8.1 8.2 8.2.1 8.2.2 8.3 8.3.1 8.3.2 8.3.3 8.4 8.4.1 8.4.2 8.4.3 8.4.4 8.4.5 8.4.6 8.4.7	Accelerated Reliability Testing111Damage Mechanisms and Failure of112Solder Attachments112Comparison of Thermal Fatigue CrackGrowth Mechanism in SAC vs. Tin/Lead BGA Solder Joints113Mixed Alloy Soldering113Solder Joints and Attachment Types115Global Expansion Mismatch116Local Expansion Mismatch116Solder Attachment Failure116Solder Attachment Failure116Solder Attachment Failure116Failure Signature-1:Cold SolderFailure Signature-2:Land, Nonsolderable117Failure Signature-3:Ball Drop117Failure Signature-5:Package Warpage118Failure Signature-6:Mechanical Failure118
8.1 8.2 8.2.1 8.2.2 8.3 8.3.1 8.3.2 8.3.3 8.4 8.4.1 8.4.2 8.4.3 8.4.4 8.4.5 8.4.4 8.4.5 8.4.6 8.4.7 8.4.8	Accelerated Reliability Testing111Damage Mechanisms and Failure of112Solder Attachments112Comparison of Thermal Fatigue CrackGrowth Mechanism in SAC vs. Tin/Lead BGA Solder Joints113Mixed Alloy Soldering113Solder Joints and Attachment Types115Global Expansion Mismatch116Local Expansion Mismatch116Solder Attachment Failure116Solder Attachment Failure116Solder Attachment Failure116Failure Signature-1:Cold SolderFailure Signature-2:Land, Nonsolderable117Failure Signature-4:Missing Ball118Failure Signature-5:Package WarpageFailure Signature-6:Mechanical FailureSignature-7:Insufficient Reflow
8.1 8.2 8.2.1 8.2.2 8.3 8.3.1 8.3.2 8.3.3 8.4 8.4.1 8.4.2 8.4.3 8.4.4 8.4.5 8.4.4 8.4.5 8.4.6 8.4.7 8.4.8 8.5	Accelerated Reliability Testing111Damage Mechanisms and Failure of112Solder Attachments112Comparison of Thermal Fatigue CrackGrowth Mechanism in SAC vs. Tin/Lead BGA Solder Joints113Mixed Alloy Soldering113Solder Joints and Attachment Types115Global Expansion Mismatch116Local Expansion Mismatch116Solder Attachment Failure116Solder Attachment Failure116Solder Attachment Failure116Failure Signature-1:Cold SolderFailure Signature-2:Land, Nonsolderable117Failure Signature-3:Ball Drop117Failure Signature-5:Package Warpage118Failure Signature-6:Mechanical Failure118Failure Signature-7:Insufficient Reflow119Critical Factors to Impact Reliability119
8.1         8.2         8.2.1         8.2.2         8.3         8.3.1         8.3.2         8.3.3         8.4         8.4.1         8.4.2         8.4.3         8.4.4         8.4.5         8.4.6         8.4.7         8.4.8         8.5         8.5.1	Accelerated Reliability Testing111Damage Mechanisms and Failure of112Solder Attachments112Comparison of Thermal Fatigue CrackGrowth Mechanism in SAC vs. Tin/Lead BGA Solder Joints113Mixed Alloy Soldering113Solder Joints and Attachment Types115Global Expansion Mismatch116Local Expansion Mismatch116Solder Attachment Failure116Solder Attachment Failure116Solder Attachment Failure116Solder Attachment Failure Classification117Failure Signature-1: Cold Solder117Failure Signature-2: Land, Nonsolderable117Failure Signature-3: Ball Drop118Failure Signature-4: Missing Ball118Failure Signature-5: Package Warpage118Failure Signature-7: Insufficient Reflow119Critical Factors to Impact Reliability119Package Technology119
8.1 8.2 8.2.1 8.2.2 8.3 8.3.1 8.3.2 8.3.3 8.4 8.4.1 8.4.2 8.4.3 8.4.4 8.4.5 8.4.4 8.4.5 8.4.6 8.4.7 8.4.8 8.5 8.5.1 8.5.2	Accelerated Reliability Testing111Damage Mechanisms and Failure of112Solder Attachments112Comparison of Thermal Fatigue CrackGrowth Mechanism in SAC vs. Tin/Lead BGA Solder Joints113Mixed Alloy Soldering113Solder Joints and Attachment Types115Global Expansion Mismatch116Local Expansion Mismatch116Solder Attachment Failure116Solder Attachment Failure116Solder Attachment Failure116Solder Attachment Failure Classification117Failure Signature-1: Cold Solder117Failure Signature-2: Land, Nonsolderable117Failure Signature-3: Ball Drop118Failure Signature-5: Package Warpage118Failure Signature-7: Insufficient Reflow119Critical Factors to Impact Reliability119Package Technology119Stand-off Height120

8.5.4	Reliability of Solder Attachments of Ceramic Grid Array	121
8.5.5	Lead-Free Soldering of BGAs	121
8.6	Design for Reliability (DfR) Process	127
8.7	Validation and Qualification Tests	128
8.8	Screening Procedures	128
8.8.1	Solder Joint Defects	128
8.8.2	Screening Recommendations	128
9 DE CA	EFECT AND FAILURE ANALYSIS ASE STUDIES	129
9.1	Solder Mask Defined BGA Conditions	129
9.1.1	Solder Mask Defined and Nondefined Lands .	129
9.1.2	Solder Mask Defined Land on Product Board	129
9.1.3	Solder Mask Defined BGA Failures	130
9.2	Over-Collapse BGA Solder Ball Conditions	130
9.2.1	BGA Ball Shape without Heat Slug 500 µm Standoff Height	130
9.2.2	BGA Ball Shape with Heat Slug 375 µm Standoff Height	130
9.2.3	BGA Ball Shape with Heat Slug 300 µm Standoff Height	131
9.2.4	Critical Solder Paste Conditions	131
9.2.5	Thicker Paste Deposit	131
9.2.6	Void Determination Through X-Ray and Cross-Section	131
9.2.7	Voids and Uneven Solder Balls	132
9.2.8	Eggshell Void	132
9.3	BGA Interposer Bow and Twist	132
9.3.1	BGA Interposer Warp	133
9.3.2	Solder Joint Opens Due to Interposer Warp	133
9.4	Solder Joint Conditions	133
9.4.1	Target Solder Condition	134
9.4.2	Solder Balls With Excessive Oxide	134
9.4.3	Evidence of Dewetting	134
9.4.4	Mottled Condition	134
9.4.5	Tin/lead Solder Ball Evaluation	135
9.4.6	SAC Alloy	135
9.4.7	Cold Solder Joint	135
9.4.8	Incomplete Joining Due to Land Contamination	135
9.4.9	Deformed Solder Ball Contamination	136
9.4.10	Deformed Solder Ball	136
9.4.11	Insufficient Solder and Flux for Proper Joint Formation	136
9.4.12	Reduced Termination Contact Area	136
9.4.13	Excessive Solder Bridging	137
9.4.14	Incomplete Solder Reflow	137

9.4.15	5 Disturbed Solder Joint 1	37
9.4.16	5 Missing Solder 1	37
10	GLOSSARY AND ACRONYMS 1	38
11	BIBLIOGRAPHY AND REFERENCES	39

# Figures

Figure 3-1	BGA package manufacturing process 2
Figure 3-2	Area array I/O position comparisons 4
Figure 3-3	Area array I/O position patterns 5
Figure 3-4	MCM type 2S-L-WB 5
Figure 3-5	Conductor width to pitch relationship7
Figure 3-6	Plastic ball grid array, chip wire bonded 8
Figure 3-7	Ball grid array, flip chip bonded8
Figure 3-8	BGA warpage 11
Figure 4-1	Partial area under the die is used to provide ground for the die. The rest of the area has been used for signal routing but has been covered with solder mask to isolate it from the conductive adhesive under the die
Figure 4-2	Use of glass die to optimize the adhesive dispensing process for void-free controlled fill and squeeze-out. The picture on the top shows the adhesive dispense pattern on the die site. The picture on the bottom shows the placed glass die to view voids and filling characteristics. The adhesive provides full die coverage for attachment but partial coverage to ground through a smaller than die ground pad, allowing a larger portion of the area under the die for signal routing saving valuable real estate and making the resulting package smaller
Figure 4-3	BOC BGA construction 15
Figure 4-4	Top of molded BOC type BGA 16
Figure 4-5	Flip-chip (bumped die) on BGA substrate 16
Figure 4-6	Plastic ball grid array (BGA) package 21
Figure 4-7	Cross-section of a ceramic ball grid array (CBGA) package
Figure 4-8	Ceramic ball grid array (CBGA) package 21
Figure 4-9	Cross-section of a ceramic column grid array (CCGA) package
Figure 4-10	Polyimide film based lead-bond µBGA package substrate furnishes close coupling between die pad and ball contact 22
Figure 4-11	Comparing in-package circuit routing capability of the single metal layer tape substrate to two metal layer tape substrate 22
Figure 4-12	Cingle peokege die steel: DOA
	Single package die-stack BGA
Figure 4-13	Custom eight die (flip-chip and wire-bond) SiP assembly
Figure 4-13 Figure 4-14	Custom eight die (flip-chip and wire-bond) SiP assembly
Figure 4-13 Figure 4-14 Figure 4-15	Single package die-stack BGA23Custom eight die (flip-chip and wire-bond)23SiP assembly23Folded multiple-die BGA package23Package-on-package FBGA24
Figure 4-13 Figure 4-14 Figure 4-15 Figure 4-16	Single package die-stack BGA23Custom eight die (flip-chip and wire-bond)23SiP assembly23Folded multiple-die BGA package23Package-on-package FBGA24SO-DIMM memory card assembly24

Figure 4-18	8 BGA connector 25
Figure 4-1	9 Example of missing balls on a BGA component
Figure 4-20	0 Example of voids in eutectic solder balls at incoming inspection
Figure 4-2	1 Examples of solder ball/land surface conditions 29
Figure 4-22	2 Establishing BGA coplanarity requirement 30
Figure 4-23	3 Ball contact positional tolerance
Figure 5-1	Examples of different build-up constructions . 32
Figure 5-2	Expansion rate above T <sub>g</sub> 34
Figure 5-3	Hot air solder level (HASL) surface topology comparison
Figure 5-4	Black pad related fracture showing crack between Nickel & Ni-Sn intermetallic layer 38
Figure 5-5	Crack location for a) black pad related failure and (b) interfacial fracture when using ENIG surface finish
Figure 5-6	Typical mud crack appearance of black pad Surface
Figure 5-7	A large region of severe black pad with corrosion spikes protruding into nickel rich layer through phosphorus rich layer underneath immersion gold surface
Figure 5-8	Graphic depiction of electroless nickel, electroless palladium/immersion gold
Figure 5-9	Graphic depiction of directed immersion gold
Figure 5-1	0 Work and turn panel layout
Figure 5-1	1 Distance from tented land clearance
Figure 5-12	2 Via plug methods 45
Figure 5-1	3 Solder filled and tented via blow-out 46
Figure 5-14	4 Metal core board construction examples 46
Figure 6-1	BGA alignment marks 47
Figure 6-2	Solder lands for BGA components 49
Figure 6-3	Metal defined land attachment profile 49
Figure 6-4	Solder mask stress concentration 49
Figure 6-5	Solder joint geometry contrast 49
Figure 6-6	Good/bad solder mask design 50
Figure 6-7	Examples of metal-defined land 50
Figure 6-8	Quadrant dog bone BGA pattern 51
Figure 6-9	Square array 52
Figure 6-1	0 Rectangular array 52
Figure 6-1	1 Depopulated array 52
Figure 6-12	2 Square array with missing balls 52
Figure 6-1	3 Interspersed array 53
Figure 6-14	4 Conductor routing strategy 53
Figure 6-1	5 BGA dogbone land pattern preferred direction for conductor routing
Figure 6-1	6 Preferred screw and support placement 55
Figure 6-1	7 Connector screw support placement 55
Figure 6-1	8 Cross section of 0.75 mm ball with via-in- pad structure (Indent to the upper left of the ball is anartifact.)

#### March 2008

Figure 6-19	Cross section of via-in-pad design showing via cap and solder ball	. 55
Figure 6-20	Via-in-pad process descriptions	56
Figure 6-21	Microvia example	56
Figure 6-22	Microvia in pad voiding	. 57
Figure 6-23	Ground or power BGA connection	. 57
Figure 6-24	Example of top side reflow joints	57
Figure 6-25	Example of wave solder temperature profile of top-aide of mixed component assembly	. 58
Figure 6-26	Heat pathways to BGA solder joint during wave soldering	. 58
Figure 6-27	Methods of avoiding BGA topside solder joint reflow	59
Figure 6-28	An example of a side contact made with a tweezers type contact	. 60
Figure 6-29	Pogo-pin type electrical contact impressions on the bottom of a solder ball	. 60
Figure 6-30	Area array land pattern testing	62
Figure 6-31	Board panelization	65
Figure 6-32	Comb pattern examples	66
Figure 6-33	Heat sink attached to a BGA with an adhesive	68
Figure 6-34	Heat sink attached to a BGA with a clip that hooks onto the component substrate	. 68
Figure 6-35	Heat sink attached to a BGA with a clip that hooks into a through-hole on the printed circuit board	. 68
Figure 6-36	Heat sink attached to a BGA with a clip that hooks onto a stake soldered in the printed circuit board	. 69
Figure 6-37	Heat sink attached to a BGA by wave soldering its pins in a through-hole in the printed circuit board	. 69
Figure 7-1	Aspect and area ratios for complete paste release	72
Figure 7-2	High lead and eutectic solder ball and joint comparison	73
Figure 7-3	Example of peak reflow temperatures at various locations at or near a BGA	. 74
Figure 7-4	Schematic of reflow profile for tin/lead assemblies	75
Figure 7-5	An example of tin/lead profile with multiple thermocouples	. 76
Figure 7-6	Schematic of reflow profile for lead-free assemblies	76
Figure 7-7	Examples of lead-free profiles with soak (top) and ramp to peak (bottom) with multiple thermocouples. The profiles with soak tend to reduce voids in BGAs	. 76
Figure 7-8	Locations of thermocouples on a board with large and small components	. 77
Figure 7-9	Recommended locations of thermocouples on a BGA	. 77
Figure 7-10	Effect of having solder mask relief around the BGA lands of the board	. 80
Figure 7-11	Flow of underfill between two parallel surfaces	82

Figure 7-12	Examples of underfill voids - small, medium and large; upper left, lower left and left of solder balls, respectively	82
Figure 7-13	Example of partial underfill - package was pulled from the PCB and dark underfill can be seen in the corners	82
Figure 7-14	Corner applied adhesive	83
Figure 7-15	Critical dimension for application of prereflow corner glue	83
Figure 7-16	Typical corner glue failure mode in shock if glue area is too low - Solder mask rips off board and does not protect the solder joints	83
Figure 7-17	Fundamentals of X-ray technology	85
Figure 7-18	X-ray example of missing solder balls	85
Figure 7-19	X-ray example of voiding in solder ball contacts	85
Figure 7-20	Manual X-ray system image quality	86
Figure 7-21	Example of X-ray pin cushion distortion and voltage blooming	86
Figure 7-22	Transmission image (2D)	86
Figure 7-23	Tomosynthesis image (3D)	87
Figure 7-24	Laminographic cross-section image (3D)	87
Figure 7-25	Transmission example	87
Figure 7-26	Oblique viewing board tilt	88
Figure 7-27	Oblique viewing detector tilt	88
Figure 7-28	Top down view of FBGA solder joints	88
Figure 7-29	Oblique view of FBGA solder joints	88
Figure 7-30	Tomosynthesis	89
Figure 7-31	Scanned beam X-ray laminography	80
Figure 7-32	Scanning acoustic microscopy	91
Figure 7-33	Endoscone example	01
Figure 7-34	Lead-free 1.27 mm pitch BGA reflowed	31
rigule 7-54	in nitrogen and washed between SMT passes	91
Figure 7-35	Lead-free BGA reflowed in air and washed	
	between SMT passes	92
Figure 7-36	Engineering crack evaluation technique	93
Figure 7-37	A solder ball cross sectioned through a void in the solder ball	93
Figure 7-38	Cross-section of a crack initiation at the ball/pad interface	93
Figure 7-39	No dye penetration under the ball	94
Figure 7-40	Corner balls have 80-100% dye penetration which indicate a crack	94
Figure 7-41	Small voids clustered in mass at the ball-to- land interface	96
Figure 7-42	X-ray image of solder balls with voids at 50 kV (a) and 60 kV (b)	97
Figure 7-43	Typical size and location of various types of voids in a BGA solder joint	98
Figure 7-44	Example of voided area at land and board Interface	98
Figure 7-45	Typical flow diagram for void assessment	100

Figure 7-46	Voids in BGAs with crack started at corner lead 104
Figure 7-47	Examples of suggested void protocols 104
Figure 7-48	Void diameter related to land size 105
Figure 7-49	X-ray image showing uneven heating 107
Figure 7-50	X-ray image at 45° showing insufficient heating in one corner of the BGA 107
Figure 7-51	X-ray image of popcorning 108
Figure 7-52	X-ray image showing warpage in a BGA 108
Figure 7-53	BGA/assembly shielding examples 109
Figure 8-1	BGA solder joint of eutectic tin/lead solder composition exhibiting lead rich (dark) phase and tin rich (light) phase grains 113
Figure 8-2	Socket BGA solder joints of SnAgCu composition, showing the solder joint comprised of 6 grains (top photo) and a single grain (bottom photo)
Figure 8-3	Thermal-fatigue crack propagation in eutectic tin/lead solder joints in a CBGA module
Figure 8-4	Thermal-fatigue crack propagation in Sn-3.8Ag-0.7Cu joints in a CBGA module [3]
Figure 8-5	Incomplete solder joint formation for 1% Ag ball alloy assembled at low end of typical process window
Figure 8-6	Solder joint failure due to silicon and board CTE mismatch
Figure 8-7	Grainy appearing solder joint 117
Figure 8-8	Nonsolderable land (black pad) 117
Figure 8-9	Land contamination (solder mask residue) 117
Figure 8-10	Solder ball down 117
Figure 8-11	Missing solder ball 118
Figure 8-12	Deformed solder joint due to BGA warping 118
Figure 8-13	Two examples of pad cratering (located at corner of BGA) 118
Figure 8-14	Pad crater under 1.0 mm pitch lead-free solder ball. Crack in metal trace connected to the land is clear; however, the pad crater is difficult to see in bright field microscopy 119
Figure 8-15A	Insufficient reflow temperature 119
Figure 8-15B	Cross-section photographs illustrating insufficient melting of solder joints during reflow soldering. These solder joints are located below the cam of a socket
Figure 8-16	Solder mask influence 121
Figure 8-17	Reliability test failure due to very large void 121
Figure 8-18	Comparison of a lead-free (SnAgCu) and tin/lead (SnPb) BGA reflow soldering profiles
Figure 8-19	Endoscope photo of a SnAgCu BGA solder ball
Figure 8-20	Comparison of reflow soldering profiles for tin/lead, backward compatibility and total lead-free board assemblies

Figure 8-21	Micrograph of a cross-section of a BGA SnAgCu solder ball, assembled onto a board with tin/lead solder paste using the standard tin/lead reflow soldering profile. The SnAgCu solder ball does not melt; black/grey interconnecting fingers are lead-rich grain boundaries; rod shape particles are Ag3Sn IMCs; grey particles are Cu6Sn5 IMCs 126
Figure 8-22	Micrograph of a cross-section of a BGA SnAgCu solder ball, assembled onto a board with tin/lead solder paste using a backward compatibility reflows soldering profile. The SnAgCu solder ball has melted

### Tables

Table 3-1	Multichip module definitions 5
Table 3-2	Number of escapes vs. array size on two layers of circuitry6
Table 3-3	Potential plating or component termination material properties
Table 3-4	Semiconductor cost predictions 11
Table 4-1	JEDEC Standard JEP95-1/5 allowable ball diameter variations for FBGA 17
Table 4-2	Ball diameter sizes for PBGAs 18
Table 4-3	Future ball size diameters for PBGAs 18
Table 4-4	Land size approximation 18
Table 4-5	Future land size approximation 18
Table 4-6	Land-to-ball calculations for current and future BGA packages (mm)
Table 4-7	Examples of JEDEC registered BGA outlines
Table 4-8	IPC-4101B FR-4 property summaries - specification sheets projected to better withstand lead-free assembly
Table 4-9	Typical properties of common dielectric materials for BGA package substrates
Table 4-10	Moisture classification level and floor life 30
Table 5-1	Environmental properties of common dielectric materials
Table 5-2	Key attributes for various board surface finishes
Table 5-3	Via filling/encroachment to surface finish process evaluation
Table 5-4	Via fill options 46
Table 6-1	Number of conductors between solder lands for 1.27 mm pitch BGAs
Table 6-2	Number of conductors between solder lands for 1.0 mm pitch BGAs
Table 6-3	Maximum solder land to pitch relationship 48
Table 6-4	Escape strategies for full arrays
Table 6-5	Conductor routing - 1.27 mm Pitch 54
Table 6-6	Conductor routing - 1.0 mm Pitch 54
Table 6-7	Conductor routing - 0.8 mm Pitch 54
Table 6-8	Conductor routing - 1.27 mm Pitch 54
Table 6-9	Conductor routing - 1.0 mm Pitch 54

Table 6-10	Conductor routing - 0.8 mm Pitch 54
Table 6-11	Effects of material type on conduction
Table 6-12	Emissivity ratings for certain materials
Table 7-1	Particle size comparisons
Table 7-2	Solder paste volume requirements for ceramic array packages
Table 7-3	Profile comparison between SnPb and SAC alloys
Table 7-4	Inspection usage application recommendations
Table 7-5	Field of view for inspection
Table 7-6	Void classification
Table 7-7	Corrective action indicator for lands used with 1.5, 1.27 or 1.0 mm pitch 101
Table 7-8	Corrective action indicator for lands used with 0.8, 0.65 or 0.5 mm pitch 102
Table 7-9	Corrective action indicator for microvia in pad lands used with 0.5, 0.4 or 0.3 mm pitch

Table 7-10	Ball-to-void size image - comparison for various ball diameters	104
Table 7-11	C=0 sampling plan (sample size for specific index value*)	106
Table 7-12	Repair process temperature profiles for tin lead assembly	111
Table 7-13	Repair process temperature profiles for lead-free assemblies	111
Table 8-1	Accelerated testing for end use environments	112
Table 8-2	Tin/lead component compatibility with lead- free reflow soldering	114
Table 8-3	Typical stand-off heights for tin/lead balls (in mm)	120
Table 8-4	Common solders, their melting points, advantages and drawbacks	123
Table 8-5	Comparison of lead-free solder alloy compositions in the Sn-Ag-Cu family selection by various consortia	123
Table 8-6	Types of lead-free assemblies possible	125

# **Design and Assembly Process Implementation for BGAs**

## 1 SCOPE

This document describes the design and assembly challenges for implementing Ball Grid Array (BGA) and Fine Pitch BGA (FBGA) technology. The effect of BGA and FBGA on current technology and component types are addressed, as is the move to lead-free assembly processes. The focus on the information contained herein is on critical inspection, repair, and reliability issues associated with BGAs. Throughout this document the word "BGA" can mean all types and forms of ball/column grid array packages.

**1.1 Purpose** The target audiences for this document are managers, design and process engineers, and operators and technicians who deal with the electronic assembly, inspection, and repair processes. The intent is to provide useful and practical information to those who are using BGAs, those who are considering BGA implementation and companies who are in the process of transition from the standard tin/lead reflow processes to those that use lead-free materials in the assembly of BGA type components.

**1.2 Intent** The new challenge in implementing BGA assembly processes, along with other types of components, is the need to meet the legislative directives that declare certain materials as hazardous to the environment. The requirements to eliminate these materials from electronic components have caused component manufacturers to rethink the materials used for encapsulation, the plating finishes on the components and the metal alloys used in the assembly attachment process.

This document, although not a complete recipe, identifies many of the characteristics that influence the successful implementation of a robust assembly process. In many applications, the variation between assembly methods and materials is reviewed with the intent to highlight significant differences that relate to the quality and reliability of the final product. The accept/reject criteria for BGA assemblies, used in contractual agreements, is established by J-STD-001 and IPC-A-610.

### 2 APPLICABLE DOCUMENTS

### 2.1 IPC1

J-STD-001 Requirements for Soldered Electrical and Electronic Assemblies

J-STD-020 Handling Requirements for Moisture Sensitive Components

**J-STD-033** Standard for Handling, Packing, Shipping and Use of Moisture/Reflow Sensitive Surface Mount Devices

**IPC-T-50** Terms and Definitions for Printed Boards and Printed Board Assemblies

**IPC-D-279** Design Guidelines for Reliable Surface Mount Technology Printed Board Assemblies

**IPC-D-325** Documentation Requirements for Printed Boards

IPC-D-350 Printed Board Description in Digital Form

**IPC-D-356** Bare Substrate Electrical Test Information in Digital Form

**IPC-SM-785** Guidelines for Accelerated Reliability Testing of Surface Mount Attachments

IPC-2221 Generic Standard on Printed Board Design

**IPC-2511** Generic Requirements for Implementation of Product Manufacturing Description Data and Transfer

**IPC-2581** Generic Requirements for Printed Board Assembly Products Manufacturing Description Data and Transfer Methodology

**IPC-7094** Design and Assembly Process Implementation for Flip Chip and Die Size Components

**IPC-7351** Generic Requirements for Surface Mount Design and Land Pattern Standard

**IPC-7525** Stencil Design Guidelines

**IPC-7711/7721** Rework, Modification and Repair of Electronic Assemblies

**IPC-9701** Performance Test Methods and Qualification Requirements for Surface Mount Solder Attachments

**IPC/JEDEC-9704** Printed Wiring Board Strain Gage Test Guideline

### 2.2 JEDEC<sup>2</sup>

**JEP95 Section 4.5** Fine Pitch (Square) Ball Grid Array Package (FBGA)

<sup>1.</sup> www.ipc.org

<sup>2.</sup> www.jedec.org