



**IPC/JEDEC J-STD-609A Errata information**  
**(These corrections should be made in user's hard copies in accordance with the company's documented control program)**

Corrections are in **green**.

**3.1 2-D code label (matrix)** A label that contains data in two dimensions as either stack or matrix types.

**3.3 base materials** The laminates and/or the prepregs used to fabricate a printed circuit board.

**3.12 Pb-free symbol** A symbol that can be used in place of the phrases s "Pb-free" or "lead-free." See Figure 4-2.

**3.14 Second (2<sup>nd</sup>) level interconnect component label** A label placed on boxes and bags that contain components with either Pb-containing or Pb-free terminal materials/ finishes. This label includes the material category and maximum component temperature (see 3.9 and 3.10). See Figure 4-3 for label formats for components with Pb-containing finishes/materials and Figures 4-4 and 4-5 for components with Pb-free finishes/materials.