IPC-4552
Amendment 1

Specification for Electroless Nickel/Immersion Gold (ENIG) Plating for Printed Circuit Boards
The Principles of Standardization

In May 1995 the IPC’s Technical Activities Executive Committee (TAEC) adopted Principles of Standardization as a guiding principle of IPC’s standardization efforts.

Standards Should:
- Show relationship to Design for Manufacturability (DFM) and Design for the Environment (DFE)
- Minimize time to market
- Contain simple (simplified) language
- Just include spec information
- Focus on end product performance
- Include a feedback system on use and problems for future improvement

Standards Should Not:
- Inhibit innovation
- Increase time-to-market
- Keep people out
- Increase cycle time
- Tell you how to make something
- Contain anything that cannot be defended with data

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Replace Table 3-1 and footnote with the following:

<table>
<thead>
<tr>
<th>Tests</th>
<th>Test Method</th>
<th>Requirement Paragraph</th>
<th>Class 1</th>
<th>Class 2</th>
<th>Class 3</th>
</tr>
</thead>
<tbody>
<tr>
<td>General</td>
<td></td>
<td>Uniform plating and complete coverage of surface to be plated</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Visual</td>
<td>Visual</td>
<td>3.1</td>
<td>Class 1 3.2.1 3 to 6 µm [118.1 to 236.2 µin]</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Electroless Nickel Thickness</td>
<td>APPENDIX 4</td>
<td>3.2.1</td>
<td>The minimum immersion gold deposit thickness shall be 0.05 µm [1.97 µin] at -4 sigma from the mean as measured on a pad size of 1.5 mm x 1.5 mm [0.060 in x 0.060 in] or equivalent area.</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Immersion Gold Thickness (Default for this IPC standard)</td>
<td>APPENDIX 4</td>
<td>3.2.2.1</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Immersion Gold Thickness (Exception required on procurement documentation)</td>
<td>APPENDIX 4</td>
<td>3.2.2.2</td>
<td>The minimum immersion gold deposit thickness shall be 0.04 µm [1.58 µin] at -4 sigma from the mean as measured on a pad size of 1.5 mm x 1.5 mm [0.060 in x 0.060 in] or equivalent area.</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Porosity</td>
<td>N/A</td>
<td>3.3</td>
<td>N/A</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Adhesion/Tape Test</td>
<td>IPC-TM-650, TM 2.4.1</td>
<td>3.4</td>
<td>No evidence of plating removed</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Solderability(2)</td>
<td>J-STD-003</td>
<td>3.5</td>
<td>Meet solderability requirements of Category 3 durability with 6 months shelf life.</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Phosphorous/Boron Content</td>
<td>ASTM B733-97 &amp; ASTM B607-91(1998)</td>
<td>1.2.1</td>
<td>(Reference Only; Supplier Dependent - No Testing Required)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Chemical Resistance</td>
<td>N/A</td>
<td>3.7</td>
<td>N/A</td>
<td></td>
<td></td>
</tr>
<tr>
<td>High Frequency Signal Loss (1)</td>
<td></td>
<td>3.8</td>
<td>TBD</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Contact Resistance (1)</td>
<td></td>
<td>1.4.2</td>
<td>TBD</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Cleanliness</td>
<td>IPC-TM-650, TM 2.3.25</td>
<td>3.6</td>
<td>Max. 1.56 µg/cm²</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

(1) An appropriate IPC-TM-650 test method used to generate data for this electrical property is not available at the time of this writing.

(2) This applies to the default Immersion Au thickness, only.
Replace Section 3.2.1 with the following:

3.2.1 Electroless Nickel Thickness  The electroless nickel thickness shall be 3 µm to 6 µm [118 µin to 236 µin] as measured on a nominal pad size of 1.5 mm x 1.5 mm [0.060 in x 0.060 in] or equivalent area, where standard feature size tolerances as expressed in the IPC-6010 standard series, apply.

NOTE: Some designs may not have feature sizes of this specified pad area and will require the use of alternate pad sizes. The XRF collimator used shall always be smaller than the pad used for measurement; and specifically, the collimator shall not exceed 30% of the feature pad size being measured. For smaller sized pads, the measurement time will need to increase proportionately as a function of collimator size reduction. Consistency of pad size measurement is of primary concern in order to obtain a statistically valid result due to the general plating deposit variation seen as a function of pad size. That is, smaller features will plate thicker than larger features. The use of fiducial pads for this measurement is suggested for designs not having the specified pad feature areas, as above.

Replace Section 3.2.2 with the following:

3.2.2 Immersion Gold Thickness

3.2.2.1 ENIG – Category 3 Coating Durability per IPC J-STD-003 – (This is the Default Gold Thickness)  The minimum immersion gold thickness shall be 0.05 µm [1.97 µin] at -4 sigma (standard deviation) from the mean as measured on a pad size of 1.5 mm x 1.5 mm [0.060 in x 0.060 in] or equivalent area, where standard feature size tolerances as expressed in the IPC-6010 standard series, apply.

The typical gold thickness range is 0.075 µm to 0.125 µm [2.95 µin to 4.92 µin]. Thicker gold than the upper end of the typical range would normally require extended solution dwell time and/or increased solution temperature. Either of these two process changes can increase the risk of compromising the integrity of the nickel undercoat due to excessive corrosion during the deposition of the immersion gold. Setup and measurement methodologies are crucial for accuracy (see APPENDIX 4 for recommended measurement techniques).

NOTE: The same considerations apply for gold thickness measurements and their relationship to pad size as applied to the electroless nickel deposition thickness. Due to the importance of control of immersion gold deposit thickness, it is imperative to have all parties measure the same feature size which may likely require discussion and agreement between user and supplier.

3.2.2.2 ENIG – Category 2 Coating Durability per IPC J-STD-003 – (This is for Soldering Applications ONLY)  The minimum immersion gold deposit thickness shall be 0.04 µm (1.58 µin) at -4 sigma from the process mean as measured on a pad size of 1.5 mm x 1.5 mm [0.060 in x 0.060 in] or equivalent area, where standard feature size tolerances as expressed in the IPC-6010 standard series, apply. The use of this reduced gold thickness shall require the following specific notation on the procurement documentation: “The gold thickness to be per Amendment 1 to IPC-4552, section 3.2.2.2.”

NOTE: Pad size concerns apply here as in 3.2.1 and 3.2.2.1.

The following four provisions shall be met for “Soldering Applications ONLY” before the ENIG plating reduction is allowed to be implemented:

1. Due to the reduced gold thickness, the printed boards shall be used within six months of date of manufacture.
2. Immersion gold plating thickness process capability shall be demonstrated.
3. Demonstrated measurement capability - A Gauge Repeatability and Reproducibility (GR&R) test shall be run showing acceptable values for the plating thickness ranges in this specification. This should be run annually or AABUS.
4. The deposit shall pass J-STD-003 solderability testing with the appropriate stressing condition of 8 hours at 72 °C ± 5 °C [162 °F ± 9 °F] and 85% ± 3% R.H. Printed boards shall show evidence of proper wetting.